

İP³XÍÓ İŞB ÀŠÒà

ç0ÍÓĚ_çYİqÁaÔçDzÄÔ% À»»RŮ»Ä ÀfÀ ÀŠÒàĚ'ÄŌÄ†İè»R×è×eŌ ÄæÑİÍ, ÄáÂSa[ÄİÄŠ
Ōà»T



×è%f%ä: ESD (Electrostatic Discharge) ÄĚÚcŌ,,Ä
Ō,,%ÄŇ_»RçĚÄ Ū€B Ō,,Ō (IC) ÄŏĚvÄ Ä Ä ÚcŌ,,Ňa
ÚÍÄŌŌ%Be»RÄnÄŌŇ"ÄēŇ]ÍŠİ' ŌŌ»RŌēŌēŌ »RŪi çc
çu%eÄpç]ÄŌŌ†ç Ä Ä ÐaĚq»TÄĚ%WŌŠÄŌÚcŌ,,Ä Ō,,
ÄİŇ}ÝrİŌ% »R×èŪ ÄÆ%fÄTÄŌŌŠÄŌĚŌÄZ»X

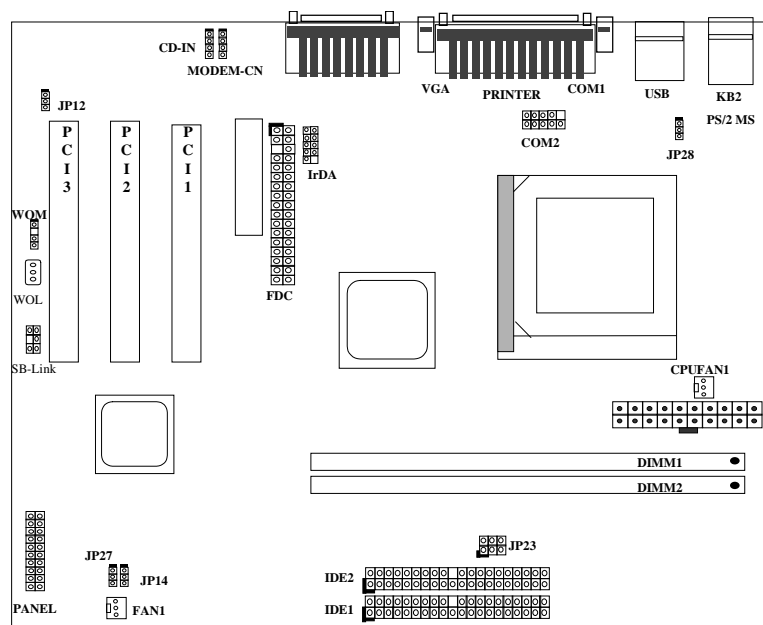
1. Ê¼ÄÜĚ' %ŠŇÄÍaÄ€Ç€Ð"Ä}ÄŠŌàŌ†ç »RÄpÄy%uŌf
%4Ç€Ě_Ō†ç ÄŏÄŌŪcŌ,,çnŌà%Ä çİ»T

2. ÄsĚ' ÄŠŌàŌ†ç Ěä»RÍæÄĚĚr%hÄÍĚİÄr×^ÄŌ%äi
Ū »R%4Í»ç ÄŌŌ,,%4Ō†ç Ä Í¼ÄÍÄs× »TÄfÄXÄdÄÍ
%äiŪ »R×èÄēçèç Ä çzçYÄŌ% ÚcŌ,,ÄŌİŪç_Ä•»RÄİ
ŌaÄ†İèŌ†ç %eçUŌ Ä`Ěİpİ»T

İŞB ÅŠ0à

2.1 Jumper 0aËÏÚj ÄÔÀ 0~

¿Y%Æ¿U0 Ä`¾Jumper Ä^ËÏÚj (connector) ÄÔË0~0é»X



İŞB ÅŠòà

Jumpers:

JP12:	Đ"Èi/Ý ÍÆÔÀøÇİÈĐİÓ%
JP14:	Ì ^ È¼CMOS
JP23:	DC/Host Clock Ratio
JP27:	SPK Out
JP28:	Ûp×]/ÑaÓÅĐ"Ø

ËİÚj:

PS2:	PS/2 ÑaÓÀËİÚj
KB:	PS/2 Ûp×] ËİÚj
COM1:	COM1 ËİÚj
COM2:	COM2 ËİÚj
PRINTER:	Printer ËİÚj
PWR2:	ATX Ó„Ñ×ËİÚj
USB:	USB ËİÚj
FDC:	Floppy ËİÚj
IDE1:	ÌĐ%QÌi IDE Í†ËİÚj
IDE2:	ÌĐ%XÌi IDE Í†ËİÚj
CPUFAN1:	CPU ÇÑÈÈËİÚj
CPU1:	ÇÑÈÈËİÚj
IrDA:	IrDA (Æ ĸ•×^ĐáÛ) Í†ËİÚj
PANEL:	Åv% ÇÈÄ` Å ÛpÓaÛ` òóÍ†ËİÚj
CD-IN:	CDROM ÇİÑ××^ËİÚj
MODEM-CN:	Mono in (Pin 1-2) Òa Mic out (Pin 3-4)
WOM:	0V Wake On Modem ËİÚj
WOL:	Wake On LAN ËİÚj
SB-LINK:	Sound Blaster Link ËİÚj

İŞB AŞÖa

2.2 Jumpers

İ, D ÇUØ Ä`Æ ÊPçèİ jumper İncf»RÊü»QÄÖ jumper Æ İ^Ê CMOS»RÄpçUÇ€Æ Çè Ä ÄeÊ`Ê\`xi ÄÖEäÇi»T

2.2.1 Ü Â CPU Úhİ%

İ, D ÇUØ Ä`ÇÇYÄöEäÊÖİ CPU Ó„Ú»R ÄYÇSÇÇYB Ê`Äs CMOS setup ÇÄİnÄŞ CPU Úhİ%»RÄİİ]DÑÄéçèÄ jumper»Tçç•»R»vçYÍ'Ó] ÇÄYİ'Ä»RÊ_ÇÜ×eÄÖ CPU ò ÊeÊ`Ü Â EEPROM Ç»TÄfÄÖ»QÄİ»RÖf»Q MOS Ü Ç»Ä»RÊ`İ_Ç»èÖü»ÇÄİn CPU Ó„Ú»Eäçi ÇÄÊÜ »R»»ÇÄEÇhÄÖÇİD"Ó„ò»İüÜaİ CPU ÖöY ÇÄ»TÄİİ, ÇÄE ÇQÉ ÊPçèİ jumper İncfÄÖ Pentium ÇUØ Ä`ÇUÇEÄÖÊ÷YU»T

İnÄŞ CPU Úhİ%ÄÖ% Ä|Æ »X

BOIS Setup à Frequency/Voltage Control à CPU FSB

(ÇZÉüÄÖİnÄŞÇaÄİ 66.8, 72, 75, 83.3, 90, 95, 100.2, 105, 107, 110, 112, 114, 117, 119, 121, 124, 125, 127, 129, 130, 133.6, 136, 138, 140, 145, 150, Öa 155 MHz.)

BOIS Setup à Frequency/Voltage Control à CPU Ratio

(ÇZÉüÄÖİnÄŞÇaÄİ 3.0x, 3.5x, 4x, 4.5x, 5x, 5.5x, 6x, 6.5x, 7x, 7.5x, Öa 8x)

CPU ÇÖİ»Úhİ% ÇÜÚh¼ x Ç•Úh

INTEL Celeron PPGA	CPU Core Frequency	CPU Ratio	CPU FSB
Celeron PPGA 300A	300MHz=	4.5x	66MHz
Celeron PPGA 333	333MHz=	5x	66MHz
Celeron PPGA 350	350MHz=	3.5x	100MHz
Celeron PPGA 366	366MHz=	5.5x	66MHz
Celeron PPGA 400	400MHz=	6x	66MHz
Celeron PPGA 433	433MHz=	6.5x	66MHz



İİÄz: INTEL 810 İÖ% İiİaÊÄçz»İÄ 100MHz CPU Ç•Úh»RÄpØ}DaÄe ÇÖİ»İ òiçè»Tİ, ÄaİnÄŞÇa»ŞDhçiİÖ% İiÄÖİhÈ »RçZÉüN"Ó ÄİèİÖÄÄ Ñ}Êq»T

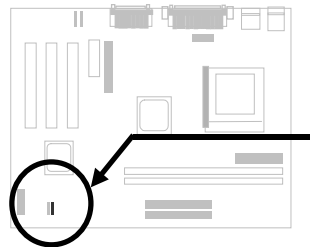
2.2.2 CPU Ó„Ú½

çŒçU0 Ä` %pİÄCeleron PPGA VID çŒú»RçzÀõÊäÊÖİ CPU Èð%üÓ„Ú½»RÂp×uÍ Às 1.3V Å 2.05V %ÄD»»T

2.2.3 İ^Ê½CMOS

JP14	İ^Ê½CMOS
1-2	çŒÛËË»Äf (ÓŠİn)
2-3	İ^Ê½CMOS

ÀfÄXË‘ ÄeÊ` Äi İnÄŠÄÖÄİİeË\×i Èä»RçİĐŒç È [aBEİ İnÄÊİ C>D»Rİ^ È½ÄYÇÄİm T^` dİ ÄÖİnÄŠÇä Äü»R%ÉúÇÄŒİĐ”Ø »T



JP14



çŒÛËË»Äf (ÓŠİn)

JP14



İ^Ê½CMOS

İ^Ê½CMOS ÄÖİ‘ Áá:

1. Ý İÄÄİİeÖ„Œ×»T
2. ÄoËÖ PWR2 %ÄÄÖATX Ó„Œ××^»T
3. Ápçİ JP14 ÄİÄsÄÖÄ Ò~»RË_D ×ÄİfÖmÄ %fÄİ»RÄ È^Ä-3 Ö”À %Ä»T
4. %QŒÄüÈäD»Äü»RÄ %fĐ ×ÄİfÖmÄŒİÈ^ ÄoÄ 1-2 Ö”À %Ä»RÄ İpÄÄÇ ÄİÄÖÄË ÖR»T
5. È_ATX Ó„Œ××^ËİÄo PWR2»T
6. ÇÄŒİçİĐ”Ö„Ö½Ö„Œ×»T
7. ÀfÄXŒbÇİİnÄŠŒİÄÖÄİİeË\×i »RçzÄsÄİİeËİÈäÈä»RÄ %f [redacted] ÜpDz% BIOS Setup İvÇË%Ä»RÄÄ ÄŠŒİÄÖË\×i »T



İ½ö: ÀfÄXË‘ ÄÖÄİİeÄnÄÊĐhÜhÄİŒ ÈÖÄeİ]Ä|Đ”Ø »RçzçYİ^Ê½ CMOS»RB ÄİİeÄoÄ ÓŠİnÄÖÄÄÖR»T

İ½ö: È½ÄÄÄçè JP14 %Äç•»RË‘ %nçzçYçè <Home> ÜpÄİ İ^Ê½ CMOS»T% Ä|Æ Ä À <Home> Üpİ^ÄüĐ”ÈİÓ„Œ×Đ”Ý »Rİ, ÖaÄİİeİ_Œ“Äö

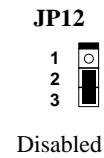
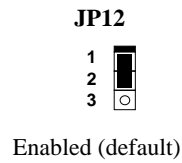
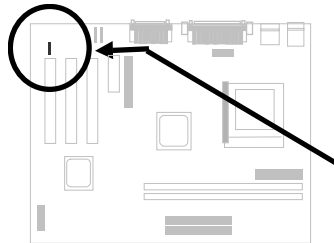
İŞB ÅŠ0à

ÊäË_ CPU ÍnÆÊ 233MHz»TËÏ%FÂi»RË' çzÀRÂæó ÔóÄÆf»RDz%[BIOS
Setup ÍnÅŠ CPU ÚhÎ%»T

2.2.4 ÇİËpİÓ¼ Đ"Ý

JP12	Sound
1-2	Enabled (default)
2-3	Disabled

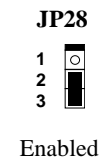
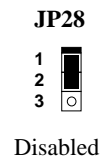
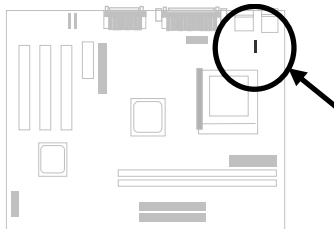
Çj Ê' %¼NbÂéçè%ÔÀòÄÔÇİËpİÓ¼ »RçzçYË_ÀÓ
jumper ÍnÆÊ Disabled»T



2.2.5 KB/MS Wakeup

JP28	KB/MS Wakeup
1-2	Disabled
2-3	Enabled

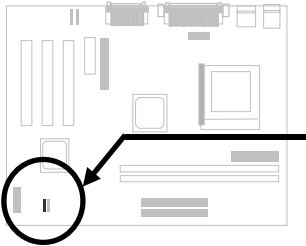
çèÁíËìçèÛp×]/ÑàÓÄĐ"Ø çñú»TÇj ÍnÆÊ Enabled»R
Ë' ÚóĐÑË,, BIOS Setup %¼ÍnÅŠĐ"Ø ÕiÀ»»TÇËÄéçè
ÀÓçñú»R5V Stand By Ó,,ÆËÏĐN¼Ä 800mA»RÃi
çYÆj ÅáÓ,,Ñ×ØÓçËúÑ"İ]Ä|Äéçè»T
×èÄqÑ_»Rç^Àí PS/2 ÑàÓÄ¼pİÄÑàÓÄĐ"Ø »T



2.2.6 SPK Out

JP27	SPK Out
1-2	Disabled
2-3	Enabled

İ, Çi jumper çèÅíĐ"ËiÃèÝ ÍÆ speaker out»T



JP27



Disabled

JP27

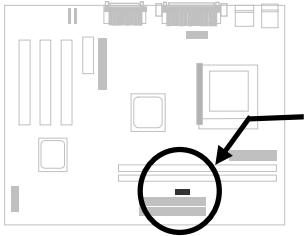


Enabled

2.2.7 DC/Host Clock Ratio

JP23	DC/Host Ratio
1-2	Auto (default)
3-4	1/1
5-6	3/2

İ, Çi jumper çèÅíÅ ÅŠ display cache Őa host clock ÅŦ
Ý Åj »T



JP23



Auto
(Default)

JP23



1/1

JP23



3/2

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Auto: 0ŞİŋÇàÆ "Auto"»TÀsÀ0ÍŋÁŠ%ƒ »R%ÔÄoÄÔdisplay cache ÄöÊäÍŋÆØRÑ ÄÔ Çà»TÄòDÍÊ'¼4ÇEBöÁ Í, ÇiÜ Đİ»T

1/1: Ê_display cache ÄÔİİÄŋ×İÄ Öa CPU ğ•ÚhÆÄa»T

3/2: Ê_display cache ÄÔİİÄŋ×İÄ 3/2 CPU ğ•Úh»T

×eÊe0mƒ Ä »T

CPU Type	66/100 signal	Bus clock	Display Cache	JP23
66MHz	Low	66MHz	100MHz	1-2
66MHz	Low	66MHz	66MHz	3-4
66MHz	Low	100MHz	150MHz	5-6
100MHz	High	100MHz	100MHz	1-2
100MHz	High	100MHz	100MHz	3-4
100MHz	High	100MHz	150MHz	5-6
100MHz	High	133MHz	100MHz	1-2
100MHz	High	133MHz	133MHz	3-4
100MHz	High	133MHz	199.5MHz	5-6

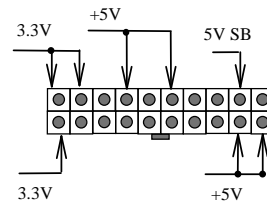
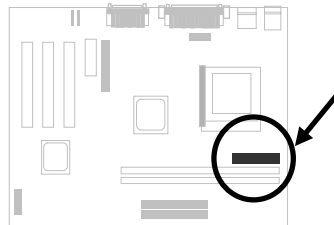
2.3 Í†ËÏÚj

2.3.1 Ó„Ñ×Í†ËÏ×^

ATX Ó„Ñ×ÅéÚÍ00Æ ÅéçèÀf%Ä Å020-pin Í†ËÏÚj »R×ê×eÃŠË'Î»%4Ä0% ÀgÆ çÜ×eÄ0»T



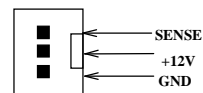
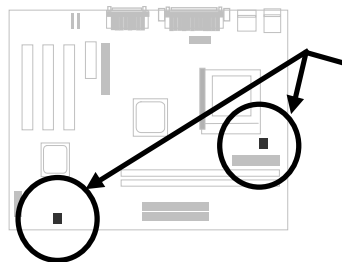
×ê%4%4i: ÀsÍ†ËÏÄeÃ0Ê!0„Ñ×Í†ËÏ×^%4Äv»R×êç Ý ÍÄ†Ìè
Ó„Ñ×»T



PWR2

2.3.2 ÇÑÈÊ

ÀsçU0 Å`%4h»RAÍ%ÇÇí0èçCPU FAN òa%ÇÇí0èçòAEFAN ÅÖÇÑÈËËÏÚj »T

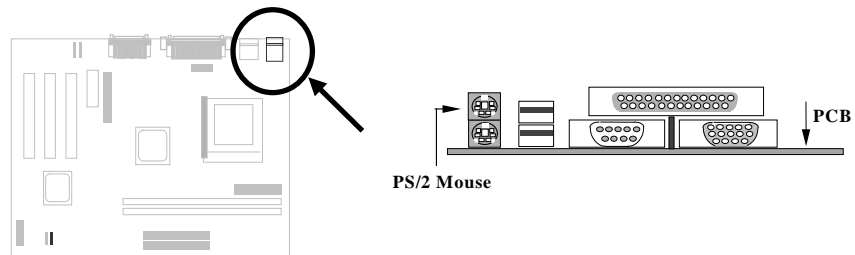


CPUFAN1
FAN1

İŞB ÅŠ0à

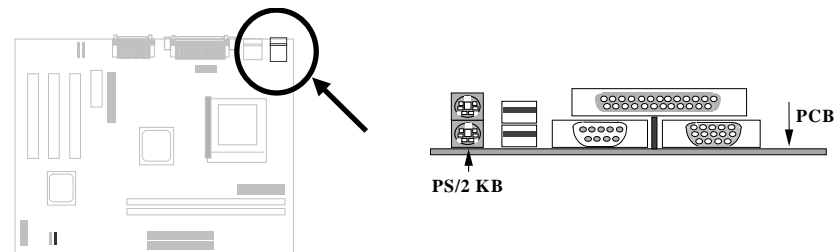
2.3.3 PS/2 ÑàÓĀ

×ēĪ†ĒĪ PS/2ĪÑàÓĀĀ ōēçöĀĒPS2 MSĪĀŌĒĪŪj ¼ĥ»T



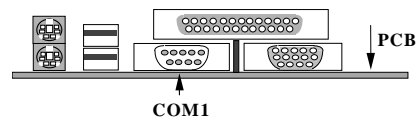
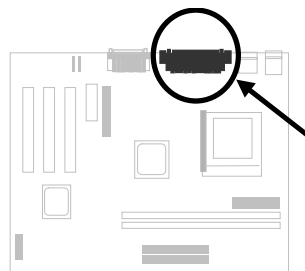
2.3.4 Ūp×]

×ēĒ_PS/2 Ūp×]ĒĪĀ ōēçöĀĒKB2 ĀŌĪ†ĒĪŪj ¼ĥ»T



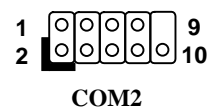
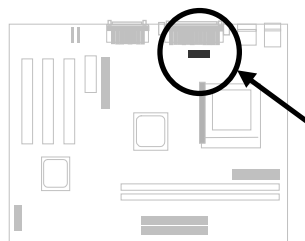
2.3.5 À ÀTÊ (COM1)

Àu% ÇËÄ` %411Í%QÇiÖë¿öÄÊ COM1 ÄÔ 9-pin D-ÄÊ ËÏÜj »R¿¿ëÄ1Í†ËÏÄ ÀTÊ ÑàÓÄ
(serial mouse) ÄëÄ Öà000 »T



2.3.6 À ÀTÊ (COM2)

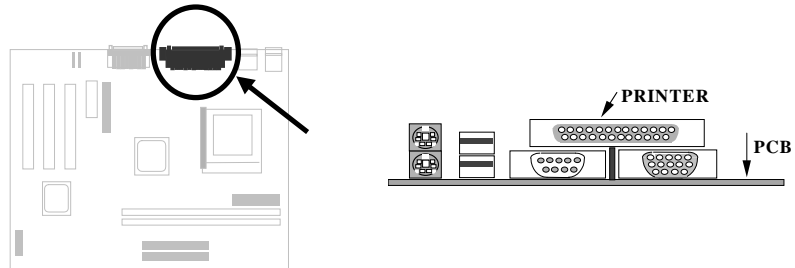
×ëË_ 10-pin ÄÔËä×`ËÏÄö COM2»T



İŞB ÅŠòà

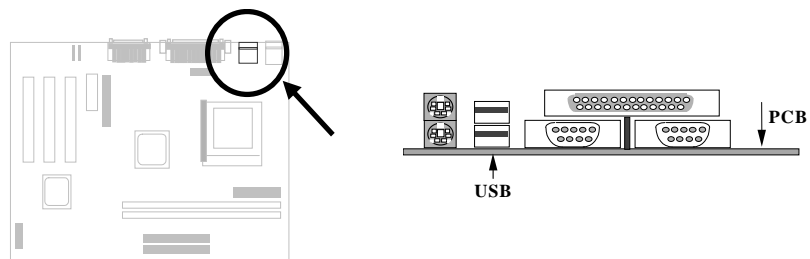
2.3.7 À]Ä Ø

çUØ Ä` Äü% ÇÄÄ` %hÄÍ%QÇiÖêö~~PRINTER~~ ÄÖ 25-pin D-ÄÄÜj »RçèÄíÄŠİ»ÄÝÄT
Ä»Ä]Ä Ø »T



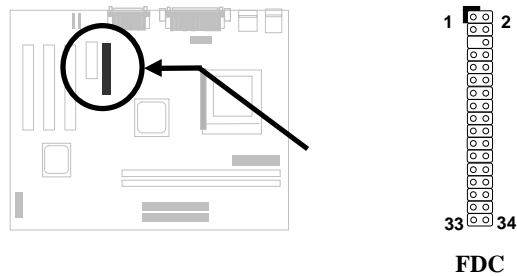
2.3.8 USB òàò~

Ë`ç~~Ä~~ USB òàò~İ†ËİÄ USB ËÜj »RÍ, D çUØ Ä` %hÄÍÄüQ1USB ËÜj »RÖêçöÄ~~Ä~~
USB»T



2.3.9 İ€ÖêØ

AsçUØ Ä` %hÄÍ%QÇiÖêçöMDC ÄÖ 34-pin EİÜj »RçzçèÄÍİ†EİÄüç<İ€ÖêØ »T

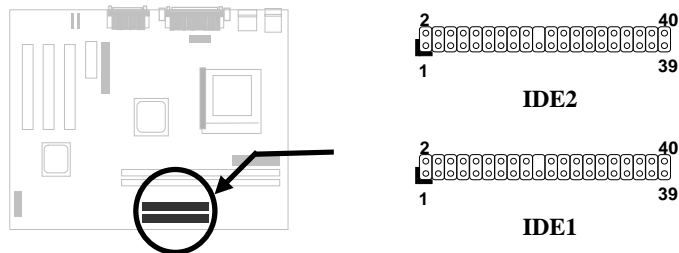


2.3.10 IDE İŞÖêØ Öa CDROM

AsçUØ Ä` %h»REİÄİÄüÇiÖêçöMIDE1 Ä^ IDE2 ÄÖ 40-pin ÈaÈS»RçzçÄÍİ†EİÄüÇi
IDE ÖaÖ~»RİæAyçİ†EİçÇi IDE ÖaÖ~»R%QÉ IDE1 %dÖöÆçUÍ„ÖU (primary
channel)»RIDE2 %dÖöÆçÄÖÍ„ÖU (secondary channel)»T

İ†EİÄ ç %QÍ„ÖUÄÖİP%Qç<ÖaÖ~çİDNİnMmaster mode»WİP%Xç<ÖaÖ~çİDNİnM
slave mode»Tç %QÇiÖaÖ~Ä»çÆİŞÖêØ Äèç ÖêØ »T

×èÈ_È'İP%Qç<ÖaÖ~İnMmaster mode ÄYÈİÄ IDE1»RİP%Xç<ÖaÖ~İnMslave
mode ÄaÖaÈİÄ IDE1»TÄfAXÈ'ÄİİP%çç<%èİPçç<»R×èÄæÄaÈİÄÄ IDE2 ÄÖ master
%è slave mode»T

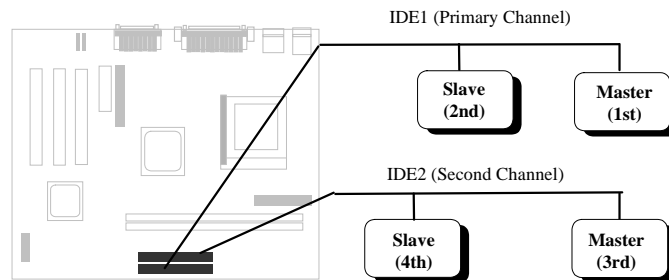


İŞB ÅŠ0à



×ē%ƒ%u: IDE İhÈ ÅoPİĒä×^İæÄ %4/zDhÓ] 46 %×
%Ů (18ÇoÀe)»RçYÁ\ò ÈaDaÛ %4/Ä”»T

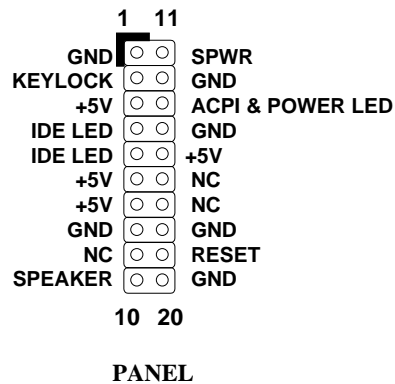
×ē%ƒ%u: ÅÊÓWÄ İæĒeÄÖAYÖÖÄ•× »RĒä×^İæÖÑÖ÷ÄÖ
òà0~İæĒİnÄÄ master mode»RÄYÄæNi %4ÖéÅoPİÄÖ
ĐĐÄáÅŠ0aÑ†òà0~»T



2.3.11 Åv% ÇĒÄ`ĒİÜj

Åv% ÇĒÄ`ĒİÜj ÅÊ20-pin ÈaÊŠ»RÖēçöäÄ
PANEL»TÄÖĒä×^ĒİÜj çACPI & Power
LED Å çöÛ` »RŮp×]Ü (keylock)»RÇÄÑ†
Đ”Ø (reset) Å Đ†»Rİüç’ (speaker)
İç»TĒ’ ççYÄæç|ÓéÄİÅŠ0à»T

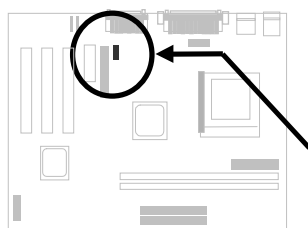
Çj Ē’ Äİ Ås BIOS %4İnÄŠ “suspend
mode”»RÄyÑ Đz% suspend ÖiÄ»Ēä»R
ACPI & Power LED Å çöÛ` Ä\ÑĐ”Ä}
Ē’Y’»T





ĀŠōāĒā»R×ēĒ_lṛDA Ē ħ×ˆōīĪāŠĪ»Ā ħU
 Ø Āˆ ¾ĥōēĀlṛDA ĀŌēĀŠ»TāŠōā¾ĀĀu»RĒ
 ŪōĭĬō÷Ḑ»ĒīISZ dldv†f1ŌĀŌĒ ħ×ˆħū»R
 ¾ ħZ; ŪĒq¾ĀQ»T

<u>Pin</u>	<u>Description</u>
1	+5V
3	FIRR (FAST IR)
4	CIRR (Consumer IR)
5	IRR (STANDARD IR)
6	5VSB
7	GND
9	IRTX (STANDARD IR)



1	○	2
3	○	4
5	○	6
7	○	8
9	○	10

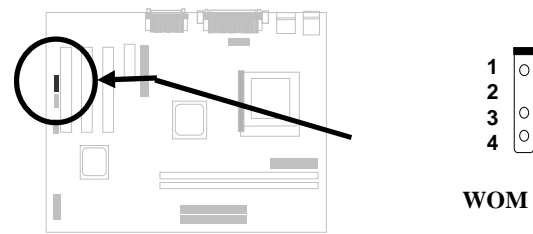
2-15

İŞB AŞÖa

2.3.13 Wake On Modem EİÜj

İÖİU0 Ä` %hÄyÄİÉdÈ x`ò İncf»Rİz%pİÄÖa060 0ÖEÉD"0
(0V Wake On Modem) İmü»R%ÖEİÄ»Äèİ•EİÄ»Öa060 Ä»
İz0Rİè»TİèÄ Äèİè%ÖEİÄ»Öa06İuÄÖ0ö»RİÇEä%Ä=ÉİD`Ö.,
N»RÄİİYÄöÇæ% ò ÄöİÉÈ'Äèİè»TÇj È'ÉDİèÄÖÆ AOpen
FM56-P»RÄy×èÄèİè 4-pin İ†Eİ x`»Rİ†Eİ FM56-P ÄÖ
RING EİÜj ÖaİU0 Ä` %hÄWOM EİÜj »T

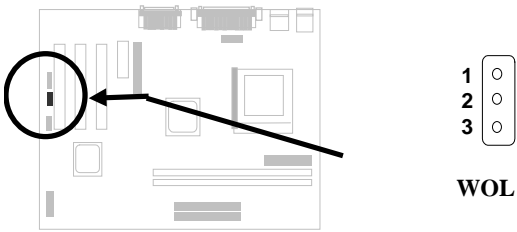
Pin	Description
1	+5V SB
2	NC
3	RING
4	GND



2.3.14 Wake On LAN EİÜj

İÖİU0 Ä` ÄyÄİWOL EİÜj »R ÇEÄèWake On LAN İm
Éü»RİİDNNÈİpİÄÄÖİmüÄÖ0 ò İuÖa0 ÖöİÉB »T

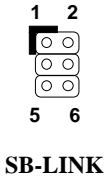
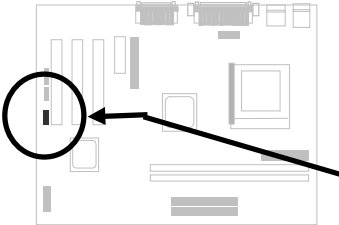
Pin	Description
1	+5V SB
2	GND
3	LID



2.3.15 Sound Blaster LINK

SB-LINK ççèÀíİ†ĖĬ Creative ÅĖñÄŦ PCI ÇĬĖPçu»T Çj Ė‘ÅŠòà¼M, ŐòÇĬĖPçu»RĬ_çĬĖÑÅçèÂ Ĭ, ÇĬĖĬÚj »RçY Å\ÅsDOS Ŭ Ői¼fÉúĖ ÅĖñ»T

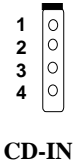
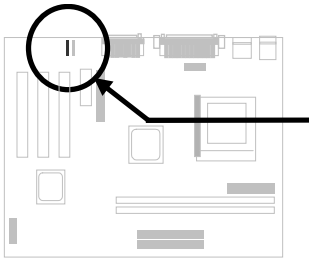
Pin	Description
1	GNT#
2	GND
3	NC
4	REQ#
5	GND
6	SIRQ#



2.3.16 CD ÇĬÑ××^ĖĬÚj

Ĭ, ÇĬĖĬÚj Å çèÀíİ†ĖĬ CDROM ÅŦÇĬÑ××^»T

Pin	Description
1	L
2	GND
3	GND
4	R

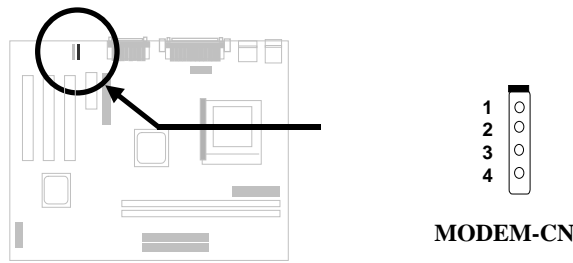


İŞB AŞÖä

2.3.16 Mono In/Mic Out ĖİÚj

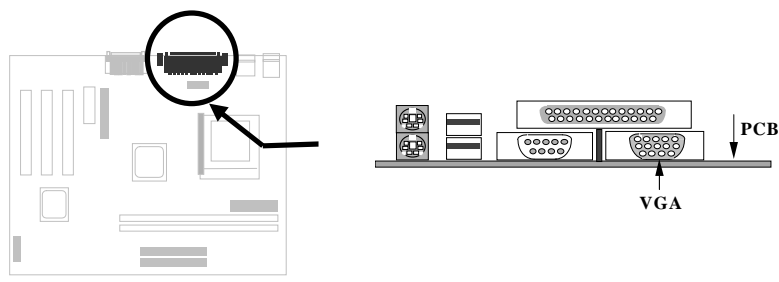
İ, Çi ĖİÚj ÇÇèĀİ İİĖİ%ÖĖİĀ»Öä0öÇuĀÖ Mono In/Mic Out ĖİÚj »TĀp%»Pin 1-2 Ė Mono In»Rpin 3-4 Ė Mic Out»TÇĖĀqĒ_ĀÖĖ »RÇöĀvİ, %QY ĀÖĖİÚj ĀYĀdĀİĀĖ ĀŠĀÖöĖĒa»RÇ^Āİ%öÖäĀÖöä0öÇuĀİİ^ĀĖİ, Çi ĖİÚj »T Ū ŪäĀv×ĖÇ ĖĖ0m|¼ ĀÖĖİÖ"ĀŠÖ,»RĀYĀgÖä0öÇuĀÖÖİĖİ ÖüĖ÷İ^Ē»»T

Pin	Description
1	Mono In
2	GND
3	GND
4	Mic Out



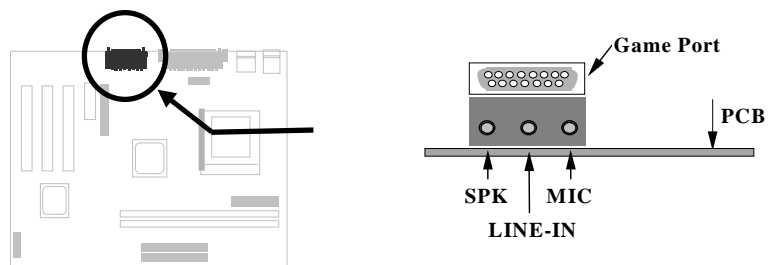
2.3.17 VGA ĖİÚj

İ, Đ ÇUÖ Ā`%ÖĀöĀİIntel İ752 ÖĖĀĖİSİ^'ÖÖÖa 4MB display cache»T

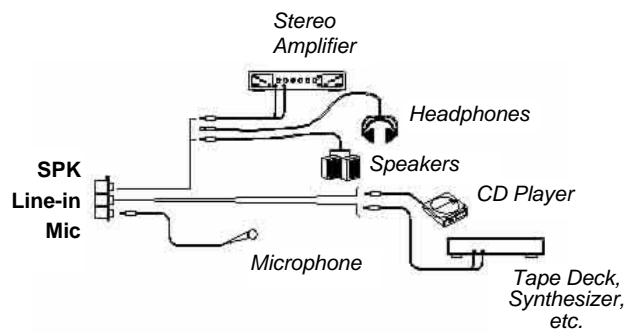


2.3.18 Audio ĖİÚj

Í, Ð ĺU0 Å`%0À%Q016-bit audio CODEC (AD1881)»T

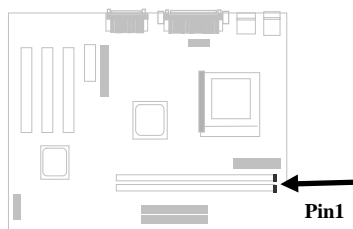


Ė' ĺĖ0Mf0eÁ1ÍĖİA0Y ÐxYpİnĖ»T



İŞB AŞÖà

2.4 AŞÖà;UE`Øêß



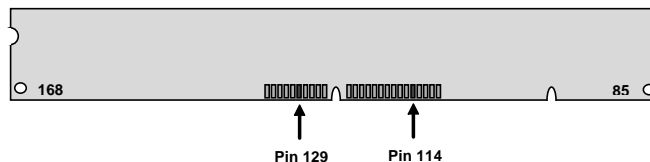
İ, D çUØ Ä`ÀÍ 2 È DIMM (Dual-in-line Memory Module) İ»Öë»R;çY³pİÄ PC100 SDRAM (Synchronous DRAM)»R İæÄÈv D,,çÖWÄ 512MB»T

DIMM Öiİi ççççY³fİi Öö¼ Ä»Êé¾X

- I. ¼¼f: İ ÇÈÆ 1Mx64 (8MB)»S2Mx64 (16MB)»S4Mx64 (32MB)»S8Mx64 (64MB)»S16Mx64 (128MB)»WÄi Ü ÇÈÆ 1Mx64x2 (16MB)»S2Mx64x2 (32MB)»S4Mx64x2 (64MB)»S8Mx64x2 (128MB)»T



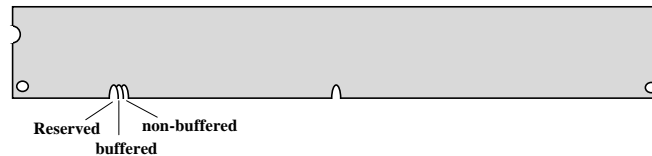
İ¼ö: ÄÍÇi¼ Ä|çççYÜaÆÈ' ÄÖ DIMM Æ İ ÇÈÜöÆ Ü ÇÈ -- ÆÆDIMM ¼hÇÈÄÖ pin 114 Öa pin 129»RÄfÄX ÄÍÊ†Ö-Ö,,Ö »RÍ, È DIMM çzÉúİ Æ Ü ÇÈÄÖ»WpÄy İ Æ İ ÇÈÄÖ»T×eÈeÑi¼ÇÈÄÖÖöeÖ»T



- II. İ†Äñ: ¼QÉ Æ ÖeçöÄf-12 İ,ÖöÄÆ»»RÍ,Ä çöÄ†Ä İ†Äñ (clock cycle time) Æ 12ns»RÄi;YÄÖSDRAM İa¼ÄÖ clock Æ 83MHz»TÜöÄÍ;¼QÖöÖeçöÆ ÄfÄa-67 İ,ÖöÄÆ»»RÄ çöÄaÄSÖ ÈaDaÜ İæÄöçÄ 67MHz»T

İŞB ÅŠòà

III. Buffered òa non-buffered: çÛçUØ Ä`%pÎÄnon-buffered DIMM»TË' ççYÄæ0ô
DIMM %hÇËËä%Ä0Ä Ò~»RÄiÄaÄ non-buffered DIMM òa buffered DIMM»T×è
ËèÑi%fÓéÄiçö»X



çëÄ Éä%Ä0Ä Ò~%Äa»Rç^ÄÍ non-buffered DIMM ççYÎ»%çUØ Ä`%hÄ0DIMM
Î»Öè»TÛ Î^ çòÄvçÄÇË%hÆ÷Ä Ä0DIMM Í1%Š0xÆ non-buffered Ä0%WRAöÇæ%ÎÄò
pÍË'ÀsÛ ÜaËäÍaÄèÜóÆ Ò ÄËËròüË÷Î^Ñ»T

IV. 2-clock òa 4-clock signals: Û Î^2-clock Ä^4-clock Ä0 DIMM Í1%ççYçèÀsÍ,
Ð çUØ Ä`%h»RÄ ÆË%WÄÏèYÇÄŠÄaÏòÑb»RAöÇæ%ÎÄòpÍË' ÍæÄèÄèç4-clock Ä0
SDRAM»T



Î1%ö: ÇèÜaÄuË' Ä0 SDRAM Æ 2-clock ÜóÆ 4-clock
Ä0»RççYÆ÷Æ÷ pin 79 òa pin 163»RAfÄXÄÍË÷0~0,,0
Î_ÜÍ0iÆ 4-clock»WpÄyÄ\Æ 2-clock Ä0»T

BIOS çZÄöËaËÖÎ È`0èß Ä0ËvÐ,%èÄÄ»R%Ä÷Äéçè Jumper ÍnÄŠ»TÍæ%Ä0Ë`0èß
ËvÐ,,Æ 512MB»T

Total Memory Size = Size of DIMM1 + Size of DIMM2

İŞB ÅŠ0à

¿Y³FÀT¿ÀoPÍÂé¿èÃ0 DRAM İiAi »X

DIMM Data chip	Bit size per side	Single/ Double side	Chip count	DIMM size	Recommended
1M by 16	1Mx64	x1	4	8MB	Yes
1M by 16	1Mx64	x2	8	16MB	Yes
2M by 8	2Mx64	x1	8	16MB	Yes
2M by 8	2Mx64	x2	16	32MB	Yes
4M by 16	4Mx64	x1	4	32MB	Yes
4M by 16	4Mx64	x2	8	64MB	Yes
8M by 8	8Mx64	x1	8	64MB	Yes
8M by 8	8Mx64	x2	16	128MB	Yes

¿Y³FÀT¿ÀoPÍÂé¿èÃ0 DRAM İiAi »X

DIMM Data chip	Bit size per side	Single/ Double side	Chip count	DIMM size	Recommended
4M by 4	4Mx64	x2	32	64MB	No
16M by 4	16Mx64	x1	16	128MB	No
16M by 4	16Mx64	x2	32	256MB	No