

# 2 Chapter 2 addendum

Table 2-5 lists the jumper settings to enable or disable the VL IDE controller.

Jumper	Function	Setting
W7	Enable VL IDE controller (default)	1-2
	Disable VL IDE controller	2-3

**Table 2-5 VL IDE Selection**

Table 2-6 lists the jumper settings to set the VL IDE hard disk controller for IRQ14. Leave this set to IRQ14 unless you are using a SCSI controller and need to free up an unnecessary IRQ, or if you want to use the ISA IDE controller as the primary IDE controller.

Jumper	Function	Setting
W33	IRQ14 (default)	1-2
	IRQ15	2-3

**Table 2-6 VL IDE IRQ14 Selection**

## Features

The LPX30WB includes the following features:

- ⊗ LPX form factor system board.
- ⊗ Micronics' X30 chipset with an on-chip external level 2 cache controller.
- ⊗ Zero Insertion Force (ZIF) socket.
- ⊗ One proprietary slot built onto the system board. Slot will accommodate a riser card which supports up to five ISA peripheral cards.
- ⊗ On-board local bus video adapter, using the Chips and Technologies 64300 video controller.
- ⊗ Local bus IDE controller with Mode 3 local bus IDE support.
- ⊗ Floppy controller (Supports 2.88MB, 1.44MB, 1.2MB, 720K, and 360K floppy drive).
- ⊗ Two high speed NS16550 compatible serial ports.
- ⊗ Bi-directional parallel port that is EPP and ECP compatible.
- ⊗ Battery-backed real-time clock.
- ⊗ Supports the following processors in the CPU ZIF socket:
  - 486SX, 25 or 33MHz
  - 486SX2, 50MHz
  - 486DX, 33MHz
  - 486DX2, 50 or 66MHz
  - 486DX4, 75 or 100MHz