

3 Jumpers and Connectors

Jumper Setting

The table below summarizes the function and jumper settings of each jumper on the SC58P VIO/S. You can refer to the next section for the graphic descriptions.

Function	Jumper Settings	
CPU Type ★	Pentium 75MHz (50MHz Host Clock)	JP14 short 1-2 JP17 open
	Pentium 90MHz (60MHz Host Clock)	JP14 short 3-4 JP17 open
	Pentium 100MHz (66MHz Host Clock) (default)	JP14 short 1-2, 3-4 JP17 open
	Pentium 120MHz (60MHz Host Clock)	JP14 short 3-4 JP17 short 1-2
	Pentium 133MHz (66MHz Host Clock)	JP14 short 1-2, 3-4 JP17 short 1-2
	Pentium 150MHz (60MHz Host Clock)	JP14 short 3-4 JP17 short 1-2, 3-4
	Pentium 166MHz (66MHz Host Clock)	JP14 short 1-2, 3-4 JP17 short 1-2, 3-4
	Cyrix 6x86-P120+ (50MHz Host Clock)	JP14 short 1-2 JP17 short 1-2
CPU Voltage	+3.3V	JP19 short 1-2
	+3.525V (VRE) (default)	JP19 short 3-4
Cache Memory Size	256KB (default)	JP13 short 1-2
	512KB	JP13 short 2-3
CPU Mode	Non-linear Mode (Intel) (default)	JP22 short 1-2

SC58P VIO/S

	Linear Mode (Cyrix)	JP22	short 2-3
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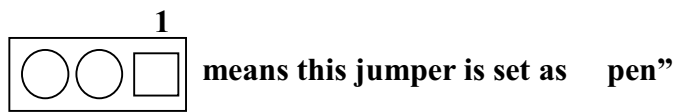
Function		Jumper Settings	
CMOS Mode	Normal (default)	J9	short 1-2
	CMOS Data Clear	J9	short 2-3
On-board VGA	Enable (default)	JP3	short
	Disable	JP3	open
BIOS Setting	Program (12V) (default)	JP11	short 1-2
	Not Program Mode (5V)	JP11	short 2-3
FDD Protect	Normal (default)	JP21	short 1-2
	Write Protect	JP21	short 2-3

Table 3 -1. Jumper Settings

☆: *The table below presents the detailed jumper settings for different CPU clock. For example, if Pentium 100MHz CPU is installed, you should set Host Clock as 66MHz and CPU Core Clock as Host Clock x 1.5.*

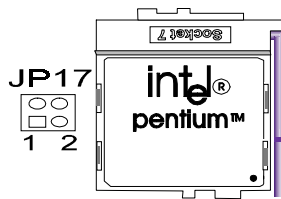
Function		Jumper Settings	
Host Clock	50 MHz	JP14	short 1-2
	60 MHz	JP14	short 3-4
	66 MHz (default)	JP14	short 1-2, 3-4
CPU Core Clock	Intel	Host Clock x 1.5 (default)	JP17 open
		Host Clock x 2	JP17 short 1-2
		Host Clock x 2.5	JP17 short 1-2, 3-4
		Host Clock x 3	JP17 short 3-4
	Cyrix	Host Clock x 2	JP17 short 1-2

Graphic Descriptions of Jumper Settings

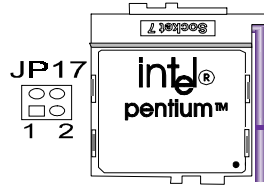


CPU Type

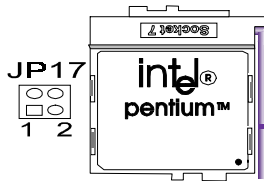
1. Intel Pentium 75MHz CPU (50MHz Host Clock) installed on board



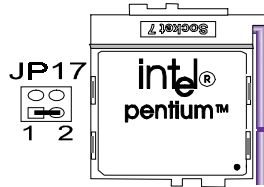
2. Intel Pentium 90MHz CPU (60MHz Host Clock) installed on board



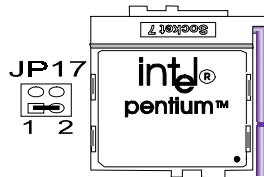
3. Intel Pentium 100MHz CPU (66MHz Host Clock) installed on board (default)



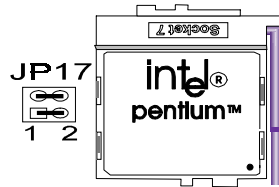
4. Intel Pentium 120MHz CPU (60MHz Host Clock) installed on board



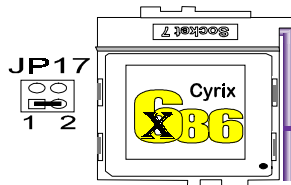
5. Intel Pentium 133MHz CPU (66MHz Host Clock) installed on board



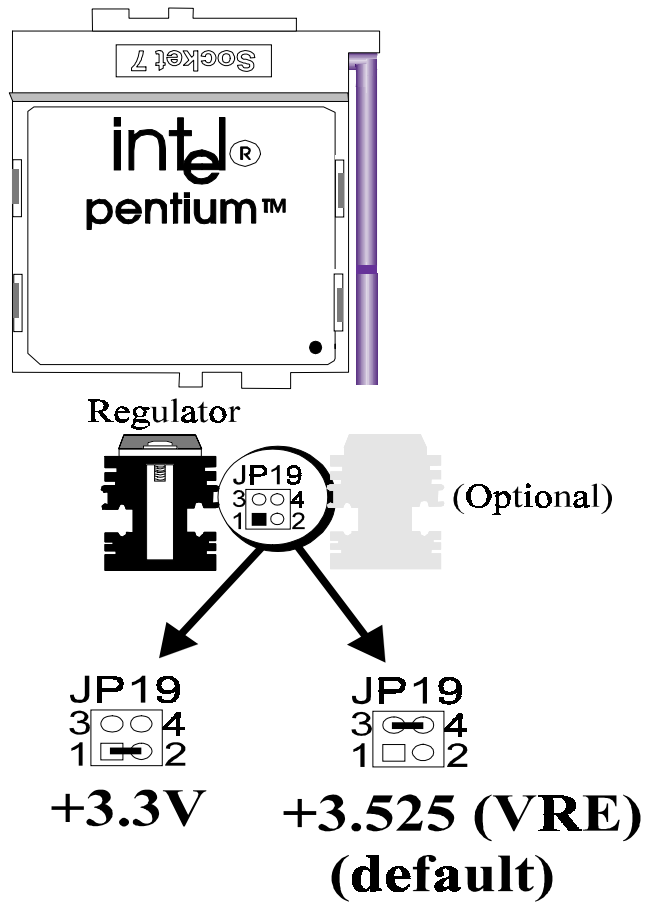
7. Intel Pentium 166MHz CPU (66MHz Host Clock) installed on board



7. Cyrix 6x86-P120+ (50MHz Host Clock) installed on board



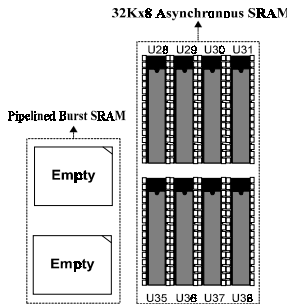
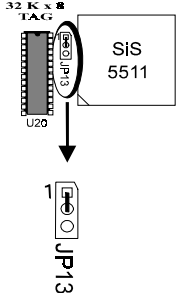
CPU Voltage



Cache Memory Size

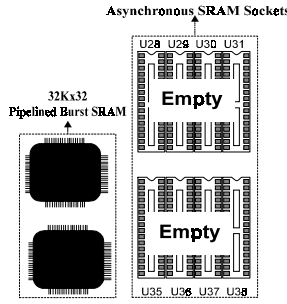
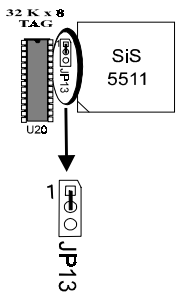
- 1. 256KB (default)

Asynchronous SRAM (mix mode)



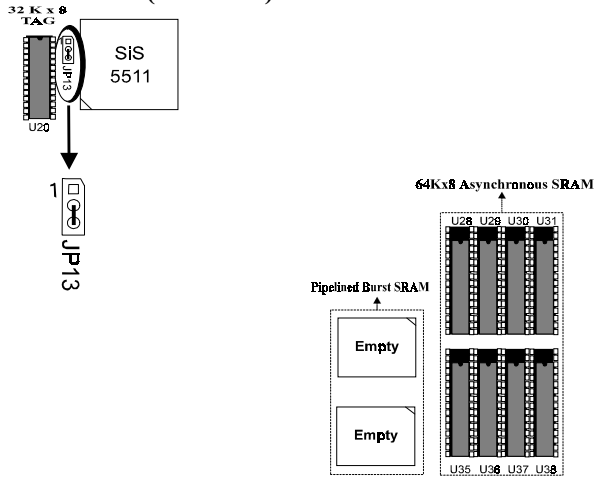
Or

Pipelined Burst SRAM

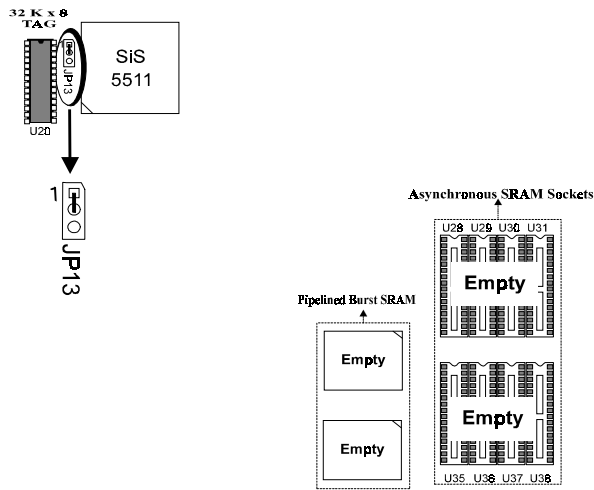


2. 512KB

Asynchronous SRAM (mix mode)



3. 0 KB



Connectors

The connectors located on the SC58P VIO/S are listed below. They can be used to connect with some peripheral devices to enhance the operating performance of the system. Please refer to the mainboard layout figure on next page for their positions.

Connector	Function
J1	PS/2 Keyboard Connector
J2	PS/2 Mouse Connector
J3	Power Connector
J5	FDD Connector
J6	Feature Connector
J7	Secondary IDE Connector
J8	Primary IDE Connector
J12	Pin 1-2: HDD LED Connector Pin 3-5: Power LED
J13	CPU Fan Connector (+12V)
J14	Turbo LED Connector
J15	Hardware Reset
JP1	IR Connector Pin 1: VCC Pin 2: NC Pin 3: RXC Pin 4: GND Pin 5: TXD
P1	Parallel Port Connector
P2	COM1 Connector
P3	COM2 Connector
P4	VGA Connector

Table 3 -2. Connectors

Board Layout

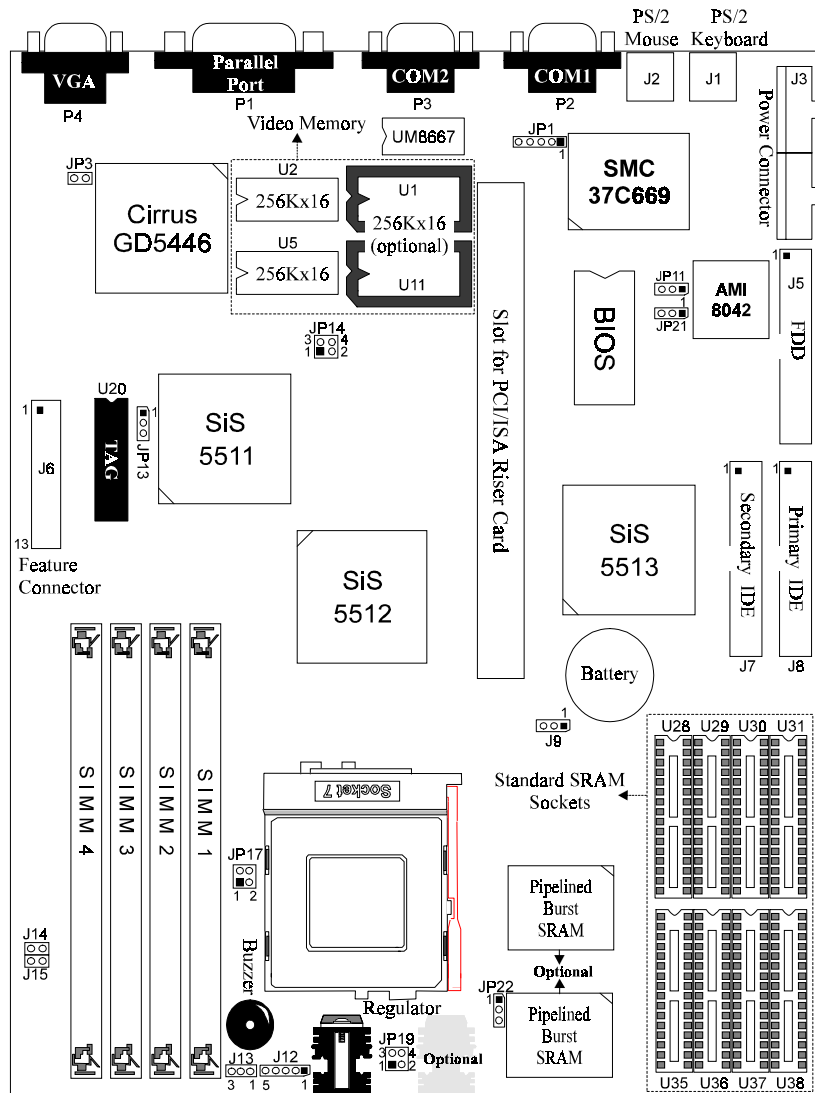


Figure 3 -1. SC58P VIO/S Mainboard Layout

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