

3 Jumpers and Connectors

Setting the Jumpers


The table below summarizes the functions and jumper settings on the TR5510 AIO. You can refer to the next section for the graphic descriptions.

Function		Jumper Settings	
CPU Type★	Pentium 75MHz (50MHz Host Clock)	J13	short 6-7, 21-22
		JP4	open
		JP8	open
	Pentium 90MHz (60MHz Host Clock)	J13	short 6-7, 21-22
		JP4	short 3-4
		JP8	open
	Pentium 100MHz (66MHz Host Clock)	J13	short 6-7, 21-22
		JP4	short 1-2, 3-4
		JP8	open
	Pentium 120MHz (60MHz Host Clock)	J13	short 6-7, 21-22
		JP4	short 3-4
		JP8	short 1-2
	Pentium 133MHz (66MHz Host Clock)	J13	short 6-7, 21-22
		JP4	short 1-2, 3-4
		JP8	short 1-2
	Pentium 150MHz (60MHz Host Clock)	J13	short 6-7, 21-22
		JP4	short 3-4
		JP8	short 1-2, 3-4
	Pentium 166MHz (66MHz Host Clock) (Reserved)	J13	short 6-7, 21-22
		JP4	short 1-2, 3-4
		JP8	short 1-2, 3-4
	Cyrix 6X86-P120+ (50MHz Host Clock)	J13	short 6-7, 21-22
		JP4	open
		JP8	short 1-2
	Cyrix 6X86-P133+ (55MHz Host Clock) (U10 uses IMI 604 only)	J13	short 6-7, 21-22
		JP4	short 1-2
		JP8	short 1-2
	Cyrix 6X86-P150+ (60MHz Host Clock)	J13	short 6-7, 21-22
		JP4	short 3-4
		JP8	short 1-2
	Cyrix 6X86-P166+ (66MHz Host Clock)	J13	short 6-7, 21-22
		JP4	short 1-2, 3-4
		JP8	short 1-2

Continued

Function		Jumper Settings
CPU Voltage	+3.3V	JP17 short 5-6
	+3.38V (default)	JP17 short 3-4
	+3.52V	JP17 short 1-2
SRAM Voltage	+5V (default) (for +3.3V/5V Mixed mode SRAM)	JP12 short 1-2 JP13 short 1-2
	+3.3V (for pure +3.3V SRAM or Pipelined Burst SRAM)	JP12 short 2-3 JP13 short 2-3
BIOS Voltage	Page Mode Flash (default)	JP23 open
	Programming Flash ROM with +5 Voltage	JP23 short 1-2
	Programming Flash ROM with +12 Voltage	JP23 short 2-3
Cache Memory Size	If using Standard SRAMs ...	
	256K (default)	JP7 short 1-2 JP21 short 2-3 JP22 short 1-2
	512K	JP7 short 2-3 JP21 short 1-2 JP22 short 2-3
	If using Pipelined Burst SRAM Module ...	
	256K and 512K	JP7 open JP21 open JP22 open
Pipelined Burst SRAM Module	TAG is installed	JP19 short 1-2
	TAG is not installed I	JP19 short 2-3
System Clock	PCI Clock/3	JP3 short 1-2
	PCI Clock/4 (default)	JP3 short 2-3
On-board Multi I/O	Enable (default)	JP1 short 1-2
	Disable	JP1 short 2-3
CMOS Mode	Normal (default)	JP2 open
	CMOS Data Clear	JP2 short

Table 3 -1. Jumper Settings


: If TAG is not installed in the Pipelined Burst SRAM Module, It is necessary to install the U17 (32Kx8 +5V Standard SRAM).

☆: The table below presents the detailed jumper settings for different CPU clock. For example, if Pentium 100MHz CPU is installed, you should set Host Clock as 66MHz and CPU Core Clock as Host Clock x 1.5.

Function		Jumper Settings
Host Clock	50 MHz	JP4 open
	60 MHz	JP4 short 3-4
	66 MHz	JP4 short 1-2, 3-4
CPU Core Clock	Host Clock x 1.5	JP8 open
	Host Clock x 2	JP8 short 1-2
	Host Clock x 2.5	JP8 short 1-2, 3-4
	Host Clock x 3	JP8 short 3-4

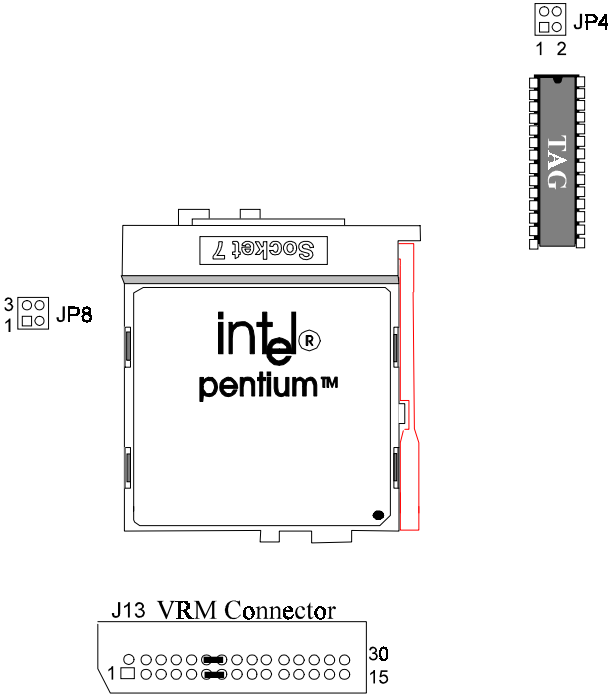
Graphic Descriptions of Jumper Settings

 means Pins 1 & 2 are set as "short"

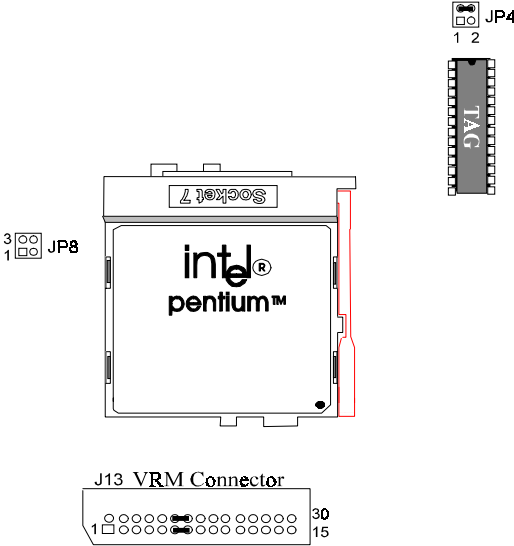
 means that this jumper is set as "open."

CPU Type

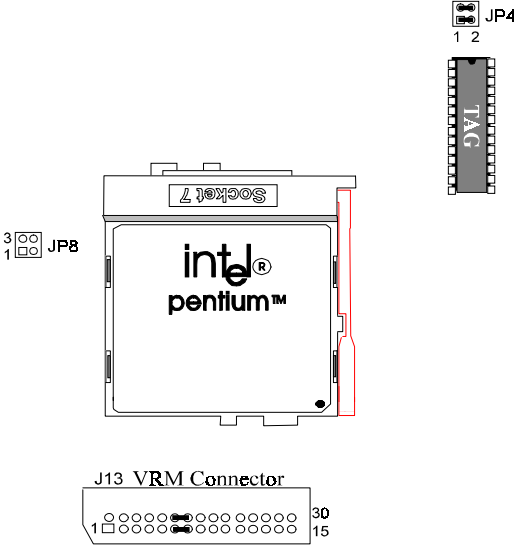
1. Pentium 75MHz (50MHz Host Clock)



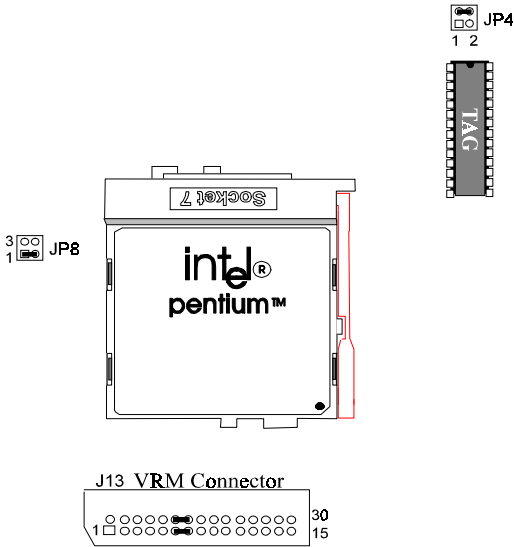
2. Pentium 90MHz (60MHz Host Clock)



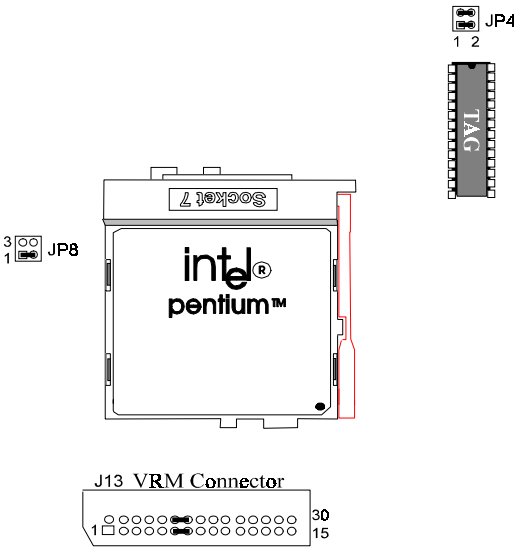
3. Pentium 100MHz (66MHz Host Clock)CPU



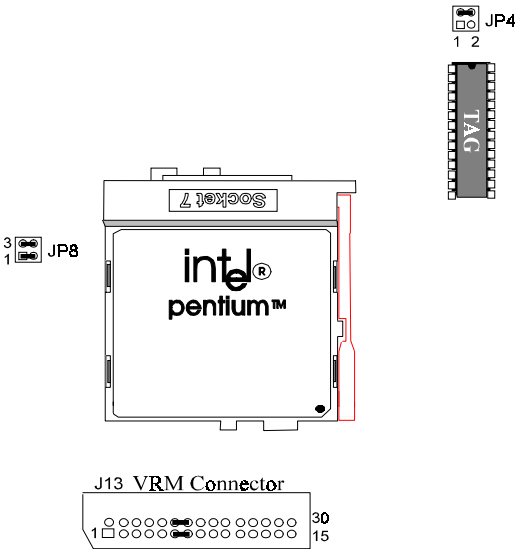
4. Pentium 120MHz (60MHz Host Clock)



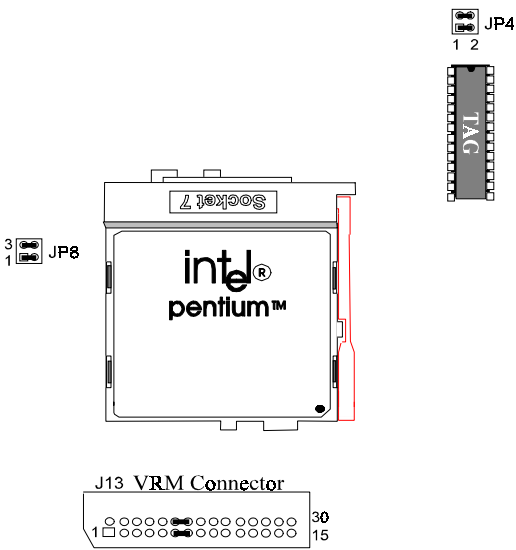
5. Pentium 133MHz (66MHz Host Clock)



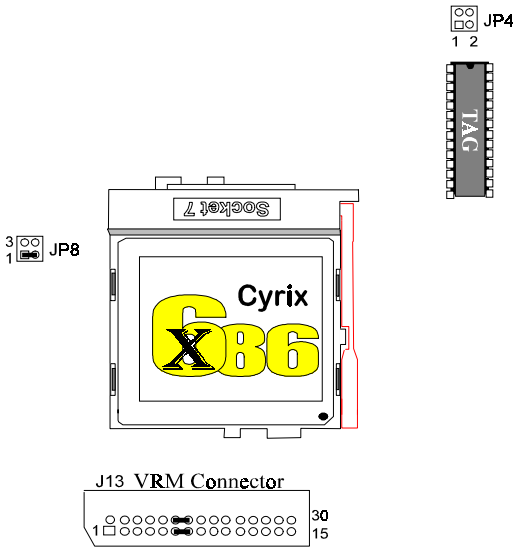
6. Pentium 150MHz (60MHz Host Clock)



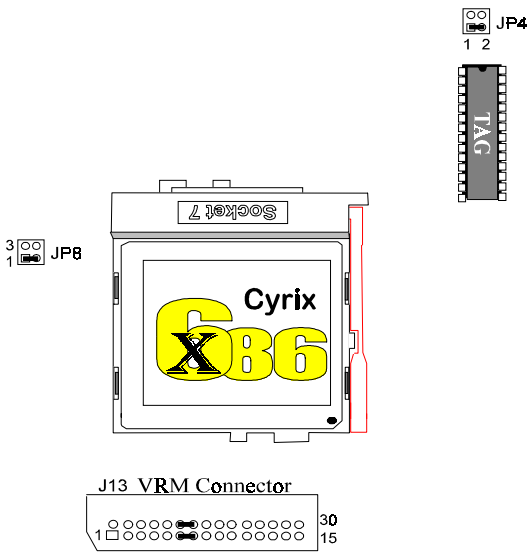
7. Pentium 166MHz (66MHz Host Clock)



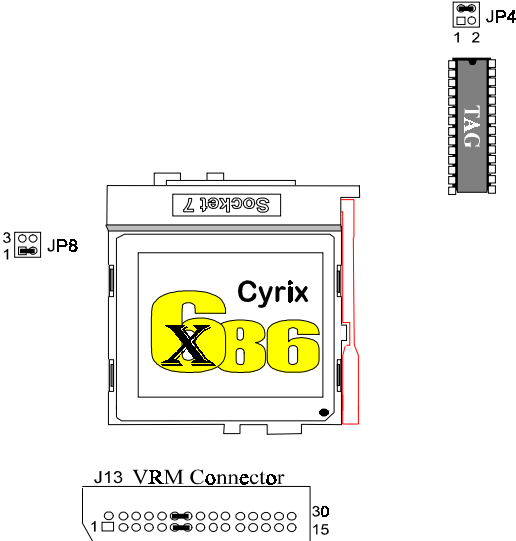
8. Cyrix 6X86-P120+ (50MHz Host Clock)



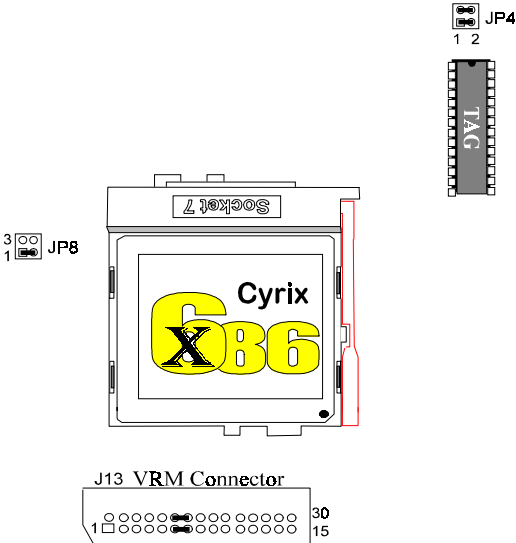
9. Cyrix 6X86-P133+ (55MHz Host Clock) (U10 uses IMI 604 only)



10. Cyrix 6X86-P150+ (60MHz Host Clock)



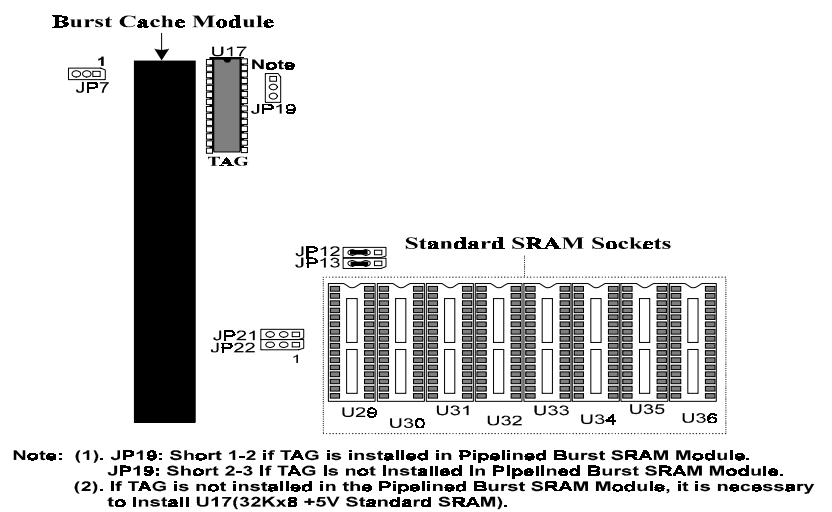
11. Cyrix 6X86-P166+ (66MHz Host Clock)



External Cache Memory Size

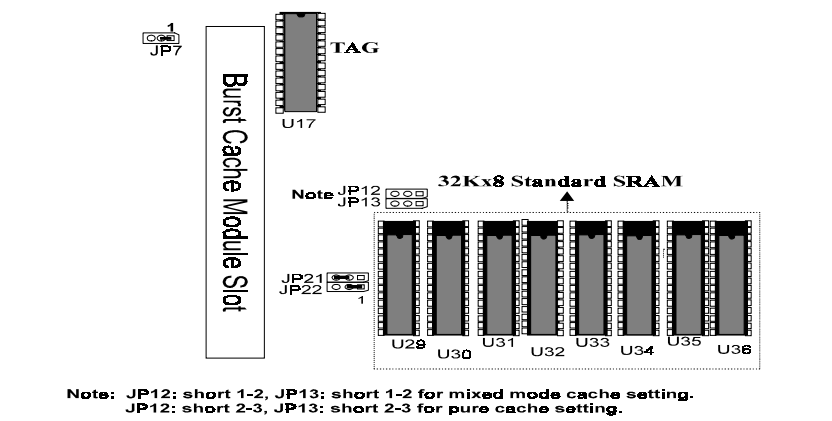
1. 256KB

Pinelined Burst SRAM



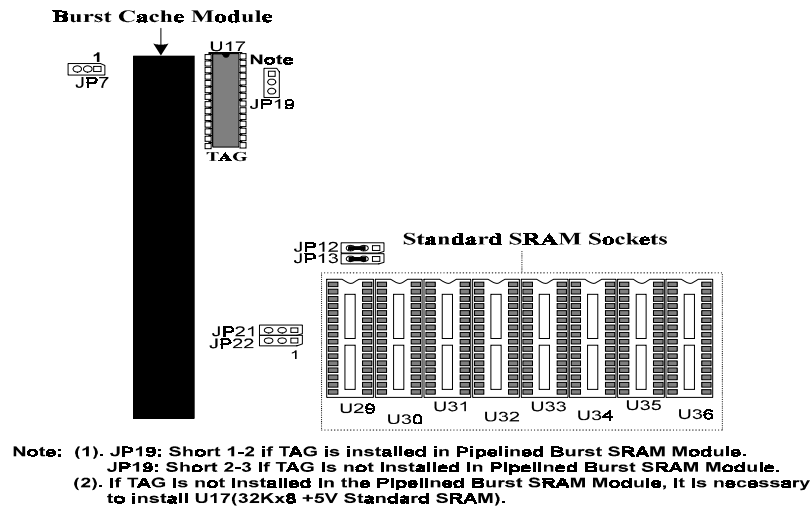
Or

Standard SRAM



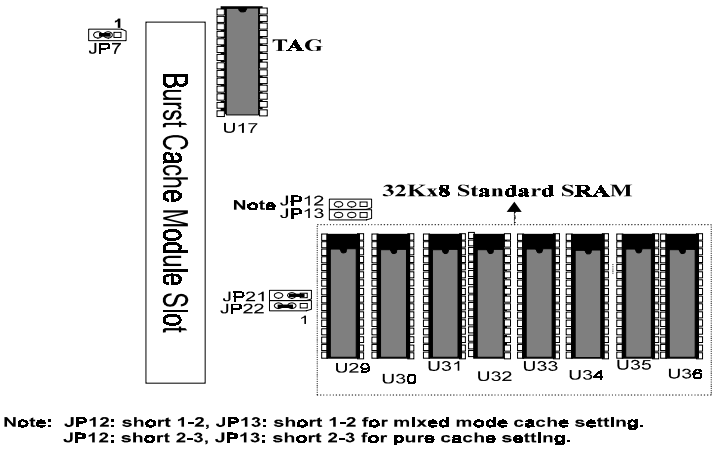
2. 512KB

Pipelined Burst SRAM

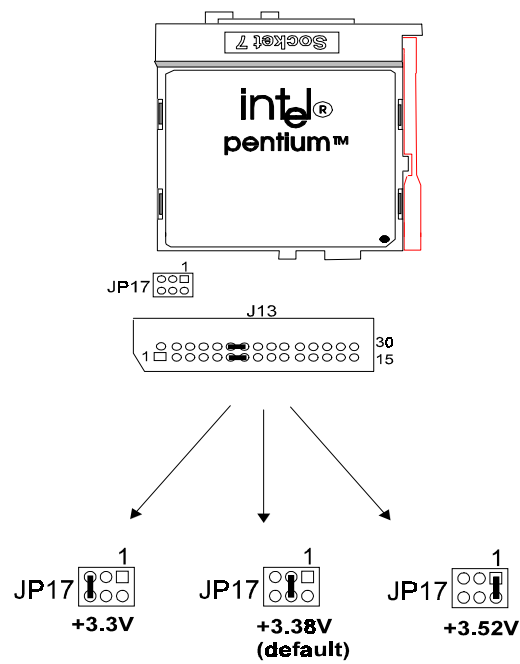


Or

Standard SRAM



CPU Voltage



Connectors

The following table lists all connector located on the TR5510 AIO. They are used to connect with some peripheral devices to enhance the operating performance of the system. Please refer to the mainboard layout figure on the next page for the positions of all the connectors.

Connector	Function																																																													
J1	PS/2 Mouse Connector (optional)																																																													
J2	AT Keyboard Connector																																																													
J3	PS/2 Keyboard Connector (optional)																																																													
J4	PS/2 Mouse Connector (5x1 Header)																																																													
J5	Serial Port 1 Connector (COM1)																																																													
J6	Parallel Port Connector																																																													
J7	Floppy Connector																																																													
J8	Serial Port 2 Connector (COM2)																																																													
J10	Secondary Hard Disk Connector																																																													
J11	Primary Hard Disk Connector																																																													
J12	<table><tr><td colspan="2">Reset Switch</td><td colspan="2"></td><td colspan="2">Turbo Switch</td><td colspan="2">SMI Switch</td><td colspan="2">Turbo LED</td><td></td></tr><tr><td>10</td><td>9</td><td>8</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td></tr><tr><td></td><td></td><td>X</td><td></td><td></td><td></td><td></td><td>-</td><td>+</td><td>X</td></tr><tr><td></td><td></td><td></td><td></td><td>X</td><td></td><td></td><td>-</td><td></td><td>+</td></tr><tr><td>20</td><td>19</td><td>18</td><td>17</td><td>16</td><td>15</td><td>14</td><td>13</td><td>12</td><td>11</td></tr><tr><td colspan="5">Speaker</td><td colspan="5">Keyboard Lock</td></tr></table>	Reset Switch				Turbo Switch		SMI Switch		Turbo LED			10	9	8	7	6	5	4	3	2	1			X					-	+	X					X			-		+	20	19	18	17	16	15	14	13	12	11	Speaker					Keyboard Lock				
Reset Switch				Turbo Switch		SMI Switch		Turbo LED																																																						
10	9	8	7	6	5	4	3	2	1																																																					
		X					-	+	X																																																					
				X			-		+																																																					
20	19	18	17	16	15	14	13	12	11																																																					
Speaker					Keyboard Lock																																																									
	X: No Function																																																													
J13	VRM Socket																																																													
J15	IR Connector																																																													
JP16	HDD LED Connector																																																													
JP26	CPU Fan Connector																																																													

Table 3 -2. Connectors

