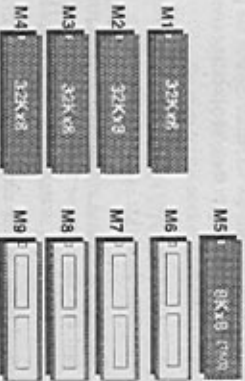
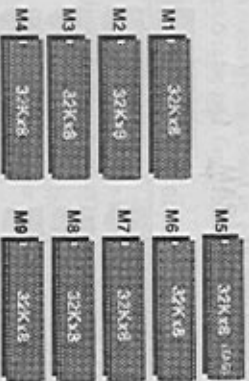


**128K Cache SRAM****256K Cache SRAM**

The cache size is jumper selectable. M1 - M4 are assigned as Bank 0 and M6 - M9 are assigned as Bank 1.

	64K	128K	256K
Bank 0	8K x 8	32K x 8	32K x 8
Bank 1	8K x 8	Empty	32K x 8
Tag RAM (M5)	8K x 8	8K x 8	32K x 8
JS1 (Jumper)	1-2	1-2	2-3
JS2 (Jumper)	1-2	2-3	2-3
JS3 (Jumper)	1-2	2-3	1-2

Table 3-2. Cache Configuration Size

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**Chapter 4****Award BIOS Setup**

The 486-GIO-VP comes with the Award BIOS chip that contains the ROM Setup information of your system. This chip serves as an interface between the CPU and the rest of the motherboard's components. This chapter explains the information contained in the Setup program and tells you how to modify the settings according to your system configuration.

**System Setup**

A Setup program, built into the system BIOS, is stored in the CMOS RAM that allows the configuration settings to be changed. This program is executed when:

1. User changes system configuration.
2. User changes system backup battery.
3. System detects a configuration error and asks the user to run the Setup program.

After power-on RAM testing, the message "**Press <DEL> to enter Setup**" appears. After pressing the above mentioned keys, the following screen appears:

ROM BIOS (2/14/2000) CMOS SETUP Utility AWARD SOFTWARE, INC.	
STANDARD CMOS SETUP	SET CMOS PASSWORD
BIOS FEATURES SETUP	SET POWER ON PASSWORD
CHIBERT FEATURES SETUP	IDE HDD AUTO DETECTION
POWER MANAGEMENT SETUP	SAVE AND EXIT SETUP
LOAD BIOS DEFAULTS	EDIT WITHOUT SAVE
LOAD SETUP DEFAULTS	
Exit: Quit	↑ ↓ → ← Select Item
F10: Save and Exit Setup	(Shift) F2: Change Color
Time, Date, Hard Disk Type	

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