


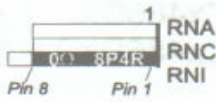


















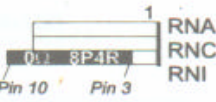



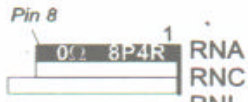






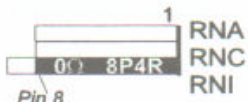



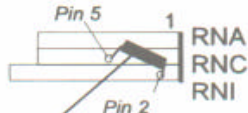





## Intel / TI / Cyrix M1sc CPUs

|                               | JC1  | JC2  | JC3  | RNA, RNC, RNI  |
|-------------------------------|--|--|--|--|
| 486SX                         |   |   |   | <br>RNA<br>RNC<br>RNI  |
| 486DX/DX2<br>P24S             |   |   |   |  |
| DX4 ODP<br>P24D               |   |   |   |  |
| DX4<br>(Triple-Speed)         |   |   |   |  |
| P24T                          |   |   |   |  |
| TI<br>Tx486DX4-G66-GA         |   |   |   |  |
| Cyrix<br>M1sc-100<br>M1sc-120 |  |  |  | <br>RNA<br>RNC<br>RNI |

## AMD CPUs

|                       | JC1   | JC2   | JC3   | RNA, RNC, RNI   |
|-----------------------|---|---|---|---|
| 486DX2                |  |  |  | <br>RNA<br>RNC<br>RNI |
| 486DX4                |  |  |  |   |
| Enhanced<br>486DX4    |  |  |  | <br>RNA<br>RNC<br>RNI |
| Enhanced<br>486DX2-80 |  |  |  | <br>RNA<br>RNC<br>RNI |
| AMD-X5                |  |  |  |   |

Install a  $0\Omega$  resistor on the pin hole 2 of RNI and the pin hole 5 of RNC.