

Memory Hole At 15MB Addr.

When enabled, every time the processor accesses the 15~16MB address, memory hole at the 15MB address will be relocated to the 15~16MB address range of the ISA cycle. When disabled, it will let the memory hole at the 15MB address decode be treated as a DRAM cycle when processor accesses the 15~16MB address.

The available options are: Enabled, Disabled (Default).

Cache Timing Control

Allows you to adjust the access speed of VT82C575MV to the external cache.

The options are: Normal, Fast, Turbo (Default).

DRAM Timing Control

Allows you to speed up the data access of VT82C575MV.

The options are: Normal, Fast (Default).

SRAM Tag/Alt Bit Config.

Allows the alter bit to check whether or not the external cache writes back data to main memory.

The options are: 7Tags+ALT (Default), 10Tags+ALT, 8 Tags.

IDE HDD Block Mode

When enabled, allows the system to execute read/write requests to hard disk in block mode.

The options are: Enabled (Default), Disabled.

IDE 32-bit Transfer Mode

When enabled, allows the system to execute read/write requests to hard disk in the speed of 32 bits per read/write cycle.

The options are: Enabled (Default), Disabled.

Onboard IDE first channel

Enable this item if the IDE hard drives use the onboard PCI IDE controller.

The available options are: Enabled (Default), Disabled.

Onboard IDE second channel

When enabled, allows you to use onboard ISA IDE controller.

The available options are: Enabled (Default), Disabled.

1st Channel IDE Master PIO

Allows automatic or manual setting of the PCI primary IDE hard disk (master) mode.

The available options are: Auto (Default), Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

1st Channel IDE Slave PIO

Allows automatic or manual setting of the PCI primary IDE hard disk (slave) mode.

The available options are: Auto (Default), Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

DRAM for BANK 0-2

Select "EDO" when you use EDO DRAMs.

The options are: Standard (Default), EDO.

Onboard FDC Control

When enabled, allows you to activate the floppy disk controller (FDC).

The options are: Enabled (Default), Disabled.