

## Addendum (25-10843-30)

### To : Users of Manual VT-503

This manual have some update needed to be noticed.

First, the Bus clock (CPU external frequency)'s jumper settings are offered to be selected by user. The table is shown right.

Tables in Page 14, 16, 18, 20 should be corrected as follows.

Bus Clock	CLK1	CLK2	CLK3
66 MHz			
60 MHz			
55 MHz			
50 MHz			









































































### Intel's Pentium/Pentium MMX CPUs

CPU Speed (MHz)	Bus Clock (MHz)	CLK1	CLK2	CLK3	Ratio	FREQ3	FREQ2	FREQ1
<b>Pentium MMX</b>								
233	66				3.5 x			
200	66				3 x			
166	66				2.5 x			
<b>Pentium</b>								
200	66				3 x			
166	66				2.5 x			
150	60				2.5 x			
133	66				2 x			
120	60				2 x			
100	66				1.5 x			
90	60				1.5 x			






































































(The rest on back page)

## VT-503 Addendum

### AMD's K5/K6 CPUs

Model	CPU Speed (MHz)	Bus Clock (MHz)	CLK1	CLK2	CLK3	Ratio	FREQ3	FREQ2	FREQ1
K6-300 *	300	66				4.5 x			
K6-266 *	266	66				4 x			
K6-233	233	66				3.5 x			
K6-200	200	66				3 x			
K6-166	166	66				2.5 x			
K5-PR200	133	66				2 x			
K5-PR166	116	66				1.75 x			
K5-PR150	105	60				1.75 x			
K5-PR133	100	66				1.5 x			
K5-PR120	90	60				1.5 x			
K5-PR100	100	66				1.5 x			
K5-PR90	90	60				1.5 x			

### Cyrix's and IBM's 6x86/6x86MX CPUs

Model	CPU Speed (MHz)	Bus Clock (MHz)	CLK1	CLK2	CLK3	Ratio	FREQ3	FREQ2	FREQ1
6x86MX-PR266*	233	66				3.5 x			
6x86MX-PR233*	200	66				3 x			
6x86MX-PR200	180	60				3 x			
	166	66				2.5 x			
	165	55				3 x			
6x86MX-PR166	150	60				2.5 x			
	138	55				2.5 x			
	133	66				2 x			
	150	50				3 x			
6x86-PR166+ 6x86L-PR166+	133	66				2 x			
6x86-PR150+ 6x86L-PR150+	120	60				2 x			
6x86-PR133+ 6x86L-PR133+	110	55				2 x	