

Appendix II

Quick Connector and Jumper Reference

CN1: WOL (Wake-on-LAN)
connector

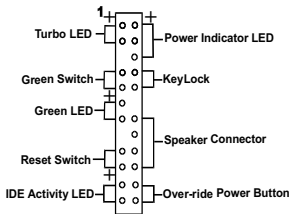


CN2: Infrared connector



CN3: Creative's SB-Link connector
links AWE64D with PCI Bus Mastering

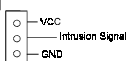
PAN1: Front panel connector set



FAN1/FAN2/FAN3: System/CPU#2 /CPU#1 cooling fan connector



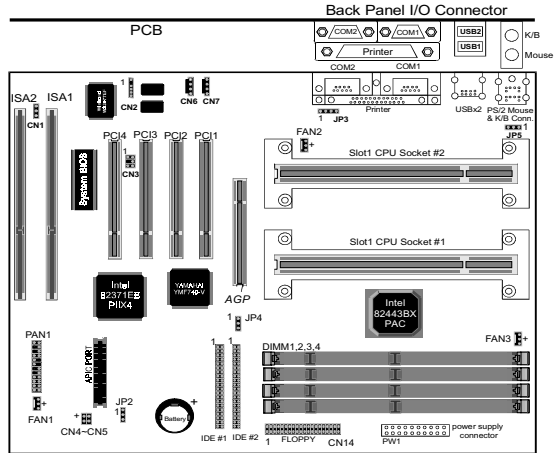
JP1: Chassis intrusion monitoring connector



JP2: Clear CMOS data jumper
1~2 : Normal (default)
2~3 : Clear CMOS data

JP3: Modem In

JP4: External clock frequency jumper
1~2 : External clock determined by CPU (default)
2~3 : External clock is always 66MHz
open : External clock is always 100MHz



SeePU (Jumper free CPU installation) setting table

CPU Type	CPU Speed			
	External Clock	Frequency Ratio	Internal	Clock
Pentium II/III Celeron Processors	66	4	266	
		4.5	300	
		5	333	
		5.5	366	
		6	400	
	100	3.5	350	
		4	400	
		4.5	450	
		5	500	
		5.5	550	

JP5: Power on Function
1~2 : Disabled (default)
2~3 : Enabled

CN4/5: External volume/Control connector

CN6: CD_IN

CN7: AUX_IN



Memo