

HARDWARE INSTALLATION















Mainboard Level Feature

1. **ISA Slot** : One 16-bit ISA Slot.
2. **PCI Slots** : Five 32-bit PCI Slots are provided.
3. **AMR Slot** : The Audio Modem Riser is a cost-effective AMR AC97/MC97.
4. **AGP Slot** : Accelerated Graphics Port (AGP).
5. **MIDI/Game Port** : 15-pin female MIDI/Joystick connector.
6. **Audio Port** : Three 1/8 female jacks for Line Out, Line In and MIC.
7. **COM2 Port** : 9-pin COM2 Ports.
8. **Parallel Port** : EPP and ECP compatible 25-pin D-Sub parallel Port
9. **COM1 Port** : 9-pin COM1 Ports.
10. **USB Ports** : Two 4-pin USB Connectors.
11. **PS/2 Mouse Port** : Supports PS/2 style mice.
12. **PS/2 Keyboard Port** : Supports PS/2 style keyboards.
13. **CPU Socket** : ZIF Socket 370 for Intel Pentium III CPU.
14. **ATX power connector** : 20-Pin ATX Power Connector.
15. **North bridge controller** : VIA 82C694X and memory controller Hub.
16. **South bridge controller** : VIA 82C686B I/O Controller Hub.
17. **SDR DIMM Sockets** : Three 168-pin DIMM Sockets.
18. **IDE1 connector** : Connector for first IDE channel.
19. **IDE2 connector** : Connector for second IDE channel.
20. **Floppy disk connector**: Built-in Floppy controller supports (2) 5.25" or 3.5" Floppy drives.
21. **USB Header** : Connector to the USB bracket for additional USB Port.

















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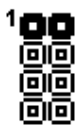













CPU Clock Jumper Selection

CPU	Clock (JP23/JP21)	Ratio (JP5)	Jumper
233MHz	66MHz	x3.5	 
266MHz	66MHz	x4.0	 
300MHz	66MHz	x4.5	 
333MHz	66MHz	x5.0	 
366MHz	66MHz	x5.5	 
400MHz	66MHz	x6.0	 
433MHz	66MHz	x6.5	 















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CPU	Clock (JP23/JP21)	Ratio (JP5)	Jumper
466MHz	66MHz	x7.0	 
500MHz	66MHz	x7.5	 
533MHz	66MHz	x8.0	 
350MHz	100MHz	x3.5	 
400MHz	100MHz	x4.0	 
450MHz	100MHz	x4.5	 
500MHz	100MHz	x5.0	 

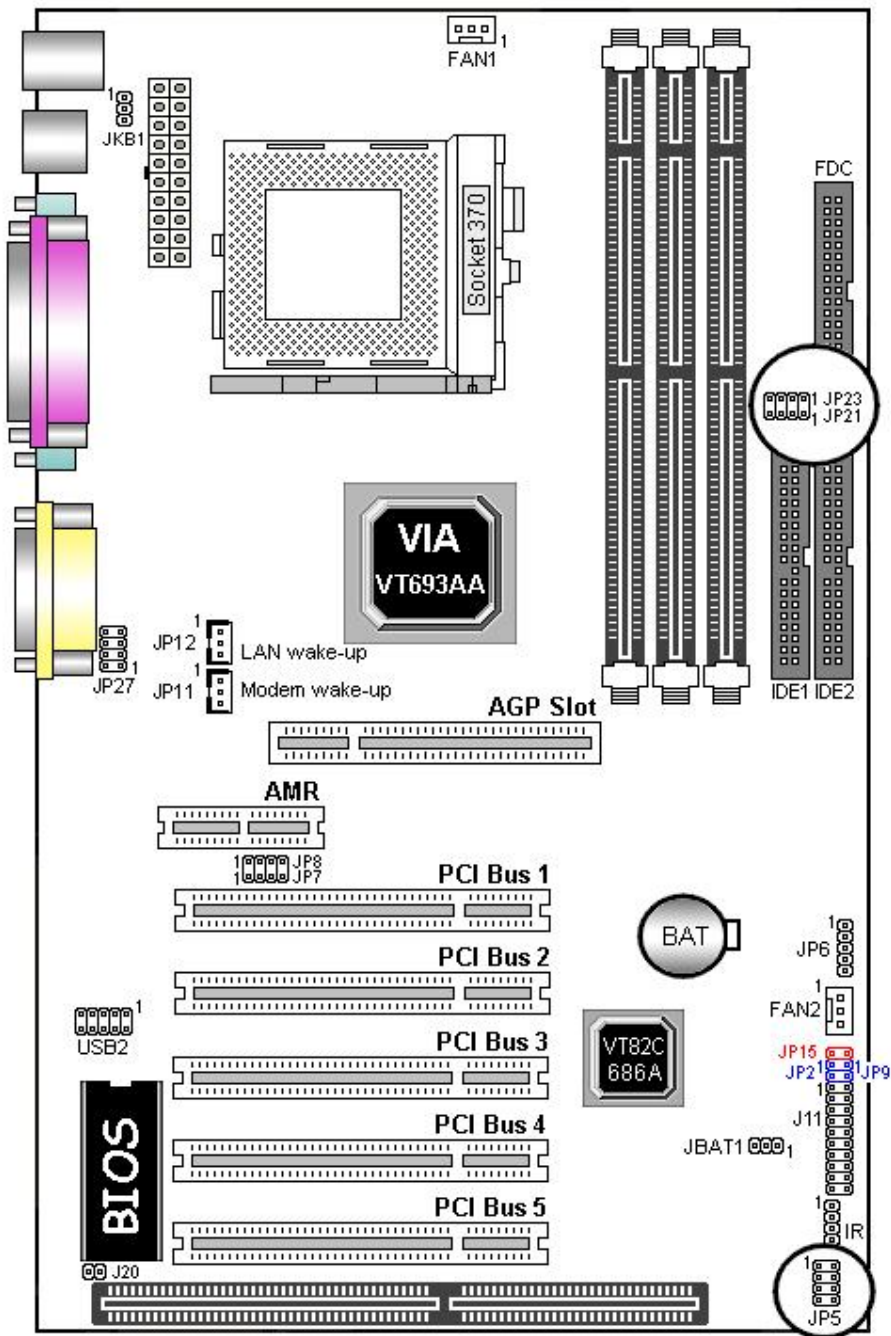
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CPU	Clock (JP23/JP21)	Ratio (JP5)	Jumper
550MHz	100MHz	x5.5	  x5.5 100
600MHz	100MHz	x6.0	  x6.0 100
650MHz	100MHz	x6.5	  x6.5 100
700MHz	100MHz	x7.0	  x7.0 100
750MHz	100MHz	x7.5	  x7.5 100
800MHz	100MHz	x8.0	  x8.0 100
533MHz	133MHz	x4.0	  x4.0 133




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CPU	Clock (JP23/JP21)	Ratio (JP5)	Jumper
600MHz	133MHz	x4.5	 
667MHz	133MHz	x5.0	 
733MHz	133MHz	x5.5	 
800MHz	133MHz	x6.0	 
866MHz	133MHz	x6.5	 
933MHz	133MHz	x7.0	 
1GHz	133MHz	x7.5	 







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



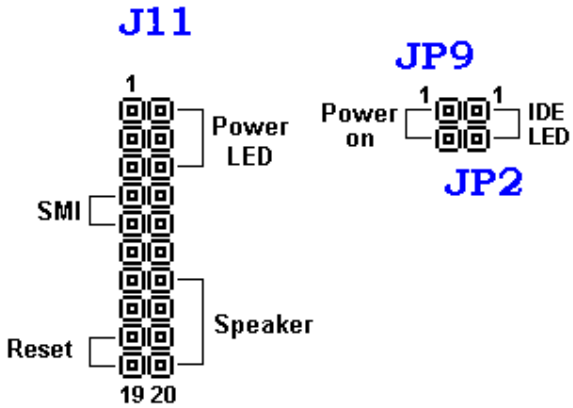
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JBAT1	Assignment
<div> <div>1</div> <div>2</div> <div>3</div> <div>  </div> </div>	Normal Operation (Default)
<div> <div>1</div> <div>2</div> <div>3</div> <div>  </div> </div>	Clear CMOS Data (None)
<div> <div>1</div> <div>2</div> <div>3</div> <div>  </div> </div>	Onboard Battery Disabled

JKB1	Assignment
<div> <div>1</div> <div>2</div> <div>3</div> <div>  </div> </div>	Normal
<div> <div>1</div> <div>2</div> <div>3</div> <div>  </div> </div>	Enabled

JP7 ; JP8	Assignment
<div> <div>1</div> <div>  </div> <div>JP8</div> </div> <div> <div>1</div> <div>  </div> <div>JP7</div> </div>	Onboard AC97 Only
<div> <div>1</div> <div>  </div> <div>JP8</div> </div> <div> <div>1</div> <div>  </div> <div>JP7</div> </div>	AMR (MC97 Only)
<div> <div>1</div> <div>  </div> <div>JP8</div> </div> <div> <div>1</div> <div>  </div> <div>JP7</div> </div>	Onboard AC97 & AMR MC97

J20	Assignment
<div> <div>  </div> </div>	Write Protect
<div> <div>  </div> </div>	Write Enable



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