# Chapter 2
Jumper Configuration

The mainboard offers a set of jumper settings to facilitate clock frequency adjustment and some important selections.

System Clock Selection

In this P6I440FX-DP Commander IV mainboard, there are two selections of SC(System Clock).User has to set a group of jumpers as the following illustration to determine which System Clock used.

JC4 JC3JC2 JC1

*System clock 60MH$_Z$:

JC4 JC3 JC2 JC1
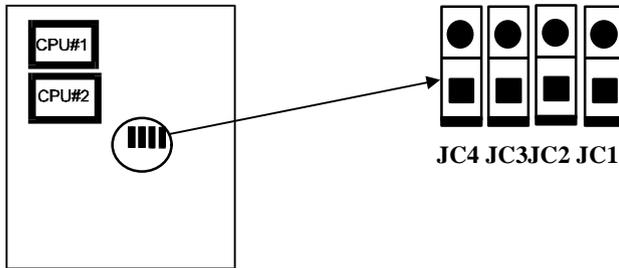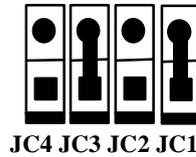
System clock 66MH$_Z$:

**JC4 JC3 JC2 JC1**
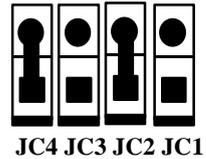
"*": Represents for the default jumper settings.

Clock Multiplier Selection

Jumper Configuration

For the Intel Pentium®Pro CPU clock multiplier, jumper settings are shown as below:

Note: SC--System Clock.

JS1~JS4

2 x SC
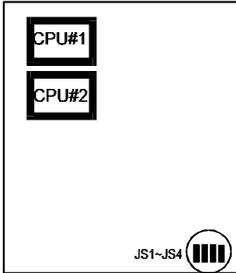
JS1~JS4
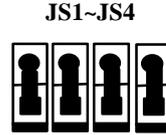
2.5 X SC
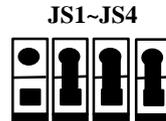
JS1~JS4

3 X SC*

JS1~JS4

3.5 X SC

JS1~JS4
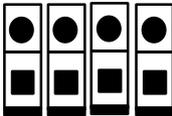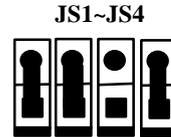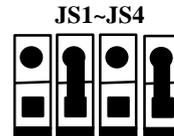
4 X SC
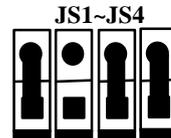
CPU Frequency Selection

According to CPU's specification, set system clock and clock multiplier carefully.The following illustration lists the jumper settings for the Pentium®Pro CPUs:

JC4 JC3JC2 JC1

JS1~JS4
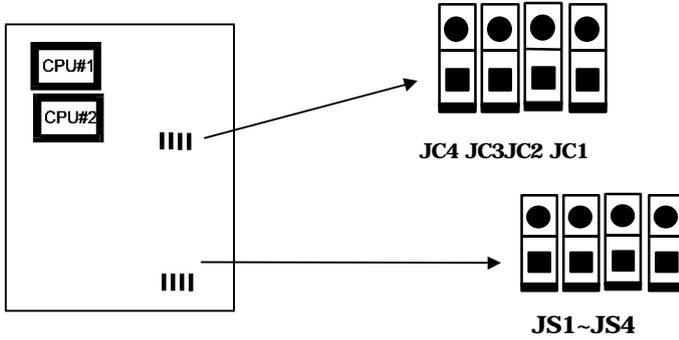
Pentium®Pro $@$150MHz = 2.5 x 60MHz:

JC4~JC1

JS1~JS4
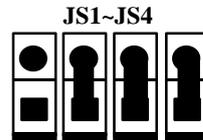
Pentium®Pro $@$166MHz = 2.5 x 66MHz:

JC4~JC1

JS1~JS4
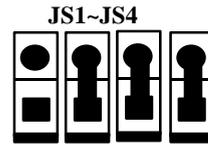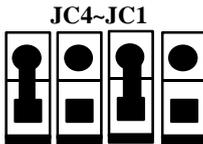
Pentium®Pro $@$180MHz = 3 x 60MHz:

JC4~JC1

JS1~JS4
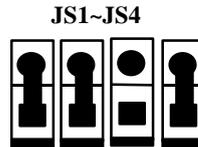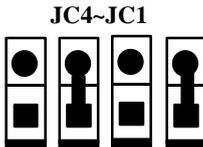
Pentium®Pro $@$200MH$_Z$ = 3 x 66MHz:

JC4~JC1

JS1~JS4
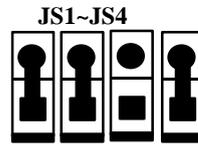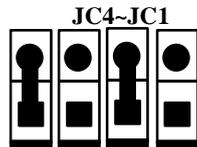
CPU Voltage Selection

To provide a wide scope of voltage for all kinds of Pentium® Pro processor CPUs, on this board there are two VRMs which transform 5V power supply into 2.1V~3.5V CPU core voltage. A set of jumpers, JA1~JA4 and JB1~JB4, give two ways to set your CPU core voltage
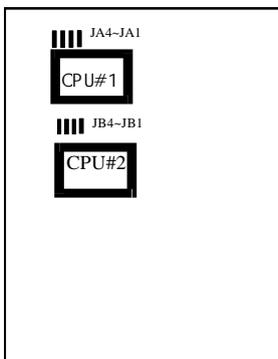
*1. Automatic Voltage Setting
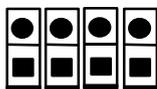
JB4~JB1
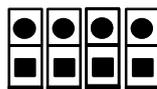
JA4~JA1

2.Manual Voltage Setting

Different CPU core voltage have different settings, JA1~JA4 are used for CPU#1 and JB1~JB4 are used for CPU#2,the following table lists all kinds of jumper setting:

CPU#1(#2) Voltage	JA1(JB1)	JA2(JB2)	JA3(JB3)	JA4(JB4)
3.5V	Close	Close	Close	Close
3.4V	Close	Close	Close	Open
3.3V	Close	Close	Open	Close
3.2V	Close	Close	Open	Open
3.1V	Close	Open	Close	Close
3.0V	Close	Open	Close	Open
2.9V	Close	Open	Open	Close
2.8V	Close	Open	Open	Open
2.7V	Open	Close	Close	Close
2.6V	Open	Close	Close	Open
2.5V	Open	Close	Open	Close
2.4V	Open	Close	Open	Open
2.3V	Open	Open	Close	Close
2.2V	Open	Open	Close	Open
2.1V	Open	Open	Open	Close
*Automatic	Open	Open	Open	Open

Note: At present, it is recommended to use Automatic Voltage Setting for Pentium®Pro CPUs, except for an older processor without "Pentium Pro" marking which does not support VID.

Clear   CMOS

Close Once

CPU#1

CPU#2

Clear CMOS

JP1

JP1

| — **JP1**   **Normal**

**Note:You must shut down power supply (220V) when
        you want to clear CMOS.**
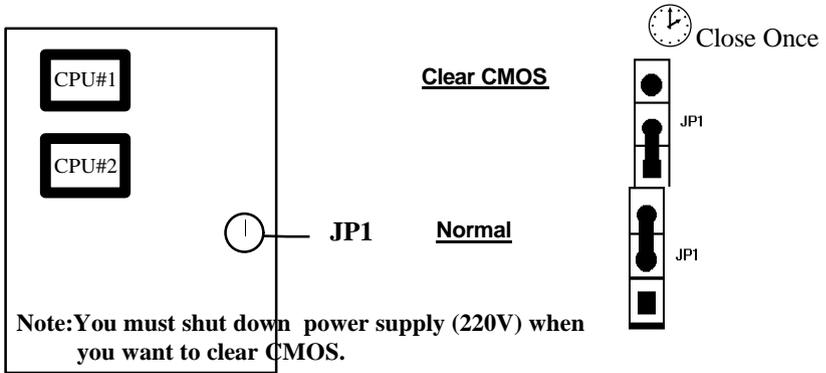
Memory Configuration

The P6I440FX-DP Commander IV mainboard supports up to eight 72Pin
SIMMs, provides a flexible size from 8MB up to 1024MB* main memory.
The DRAM SIMMs can be installed into any bank. You should plug in two
same type and brand of SIMM modules on a bank simultaneously.
The minimum SIMM size is 4MB; the maximum SIMM size is 128MB.

SIMM8	**Bank 4**
SIMM7	
SIMM6	**Bank 3**
SIMM5	
SIMM4	**Bank 2**
SIMM3	
SIMM2	**Bank 1**
SIMM1	

"*": Just according to chipset specification.