






Expansion Slots & I/O Ports description

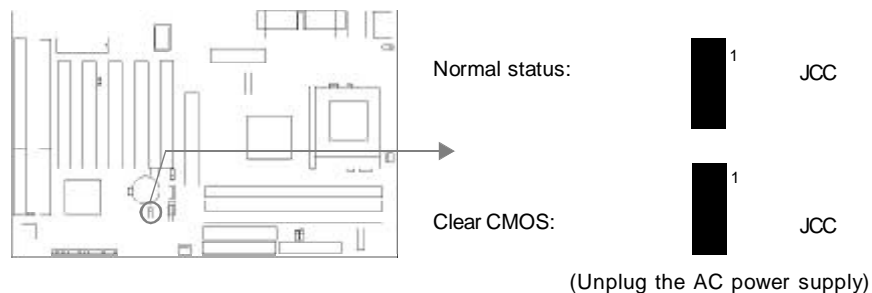
Slot / Port	Description
ISA 1	First ISA slot.
ISA 2	Second ISA slot.
PCI1	First PCI slot.
PCI2	Second PCI slot.
PCI3	Third PCI slot.
PCI4	Fourth PCI slot.
PCI5	Fifth PCI slot.
IDE 1	Primary IDE port.
IDE 2	Secondary IDE port.
FLOPPY	Floppy Drive Port.
AGP	Accelerated Graphics Port.

Jumper Settings

Jumpers are located on the mainboard, they represent, clear CMOS jumper JCC, enable keyboard password power-on function jumper JKB etc. Pin 1 for all jumpers are located on the side with a thick white line (Pin1→ ), refer to the mainboard' s silkscreen . Jumpers with three pins will be shown as  to represent pin1 & pin2 connected and  to represent pin2 & pin3 connected.

Clear CMOS (JCC)

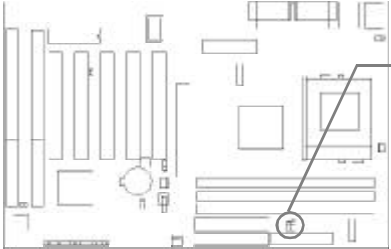
If you want to clear CMOS, unplug the AC power supply first, close JCC (pin1 & pin2) once, set JCC back to the normal status with pin2 & pin3 connected, then power on the system.





Overclocking Jumper Setting (JFSB1, JFSB2)

Jumpers labeled JFSB1, JFSB2 are located on the mainboard providing users with CPU overclocking feature. The host bus speed can be set as 66/100/133MHz. Refer to the chart below for the location of these jumpers, and the table for information on how to set them.

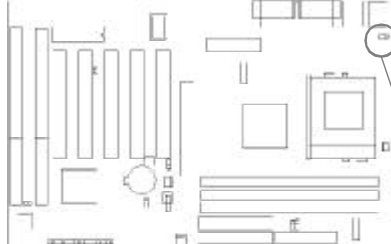


CPU FSB	JFSB2		JFSB1	
	1	2	1	2
66MHz	Pin2&Pin3 closed		Pin2&Pin3 closed	
100MHz	open		Pin2&Pin3 closed	
133MHz	open		open	
Auto	Pin1&Pin2 closed		Pin1&Pin2 closed	

If CPU FSB is set as default setting AUTO, the system detects the CPU front side bus automatically and run processor actual front side bus. If CPU FSB is set as 66MHz, the system only run at 66MHz front side bus even if a processor with 100MHz or higher. If CPU FSB is set as 100MHz, the system can run at 100MHz front side bus even if a processor with 66MHz FSB is installed. Setting up to 133MHz FSB is also supported. However, whether or not your system can be overclocked depends on your processor's capability. Whether the processor is bus ratio locked or unlocked should also be taken into account. For bus ratio unlocked processor, this overclocking feature can be implemented by setting JFSB1 and JFSB2 as all opened, meanwhile respectively adjusting the bus clock higher and the bus ratio (Multiplier) lower in "SpeedEasy CPU Setup" in AWARD BIOS CMOS Setup. We do not guarantee the overclocking system to be stable.

Enable keyboard password power-on function (JKB)

The mainboard provides the advanced keyboard password power-on function. When wanting to use this function, set JKB with pin1& pin2 closed. Otherwise, set JKB with pin2 & pin3 closed for disabling this function.



Disable:	JKB	3	2	1
Enable:	JKB	3	2	1

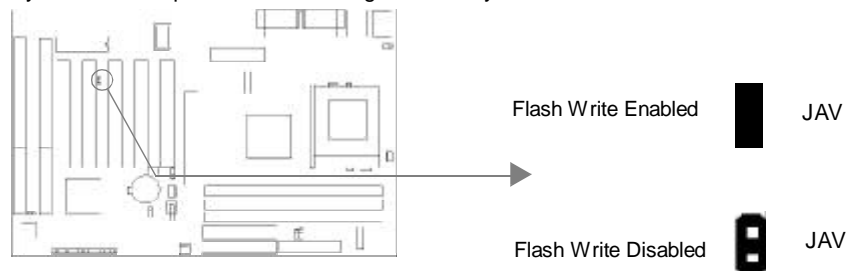
In order to implement this function, set "POWER ON Function" to **Password/Button** or **Password** and set the keyboard power-on password in the "INTEGRATED PERIPHERALS" section of the BIOS. For detailed information, see the explanations in "INTERGATED PERIPHERALS" section of BIOS part.



Note: 1.If wanting to use this function, 5VSB line of the power supply should be capable of delivering enough current (eg. 200mA) for all devices connected to the keyboard port, or you will be unable to power up the system using the keyboard.
 2.If you set JKB with pin2 & pin3 closed, set "POWER ON Function" to Password/Button or BUTTON ONLY, don't set it to Password, or this will prevent you from powering up your system.
 3. If you encounter the above problems, clear CMOS and reset the jumper and BIOS option.

BIOS-ProtectEasy Jumper (JAV)

The BIOS of the mainboard is contained inside the Flash ROM. If the jumper JAV is set as closed, you will be unable to flash the BIOS to the mainboard. However in this status, the system BIOS is protected from being attacked by serious virus such as CIH virus.



Setting the jumper JAV as opened(default), meanwhile disabling the "Flash Write Protect" item from "BIOS Features Setup" in AWARD BIOS CMOS Setup, allows you to flash the BIOS to the Flash ROM.

The DMI (Desktop Management Interface) system information such as the CPU type/speed, memory size, and expansion cards will be detected by the onboard BIOS and stored in the flash ROM. Whenever the system hardware configuration is changed, DMI information will be updated automatically. However, setting jumper JAV as closed makes flashing BIOS and updating DMI information impossible. Therefore, set JAV as opened when changing the system hardware configuration, or the error message "Unkown Flash Type" will be displayed on the screen, and DMI information update will be fail.

Setting the jumper JAV as opened(default), meanwhile disabling the "Flash Write Protect" item from "BIOS Features Setup" in AWARD BIOS CMOS Setup, allows you to flash the BIOS to the Flash ROM.

Setting the jumper JAV as closed, the Pentium®III Processor Number can be not readable whatever disabling or enabling "Processor Number Feature" item from "BIOS Features Setup" in AWARD BIOS CMOS Setup.



Memory Configuration

This mainboard provides three 168 pin 3.3V un-buffered/buffered DIMM sockets to support a flexible memory size ranging from 8MB up to 768MB for SDRAM or from 8MB up to 768MB for EDO memory. Both PC66/PC100/PC133MHz SDRAM with SPD and 66MHz EDO DIMMs are supported. The following set of rules allows optimum configurations.

- Using the serial presence detect (SPD) data structure, programmed into an E²PROM on the DIMM, the BIOS can determine the SDRAM's size and speed.
- The DRAM Timing register, which provides the DRAM speed grade control for the entire memory array, must be programmed to use the timing of the slowest DRAMs installed.
- Possible SDRAM DIMM memory sizes are 16MB, 32MB, 64MB, 128MB, 256MB in each DIMM socket.
- Possible EDO DIMM memory sizes are 16MB, 32MB, 64MB, 128MB, 256MB in each DIMM socket.
- Processor with 66MHz FSB should be paired with PC66, PC100, PC133 SDRAM
processor with 100MHz FSB should be paired with either PC100 or PC133 SDRAM,
processor with 133MHz FSB should be paired only with PC133 SDRAM.



Chapter 3

BIOS Description

Utility Support:

AWDFLASH.EXE

This is a flash memory write/read utility used for the purpose of upgrading your BIOS when necessary. Before doing so, please note:

- **We strongly recommend you only upgrade BIOS when encounter problems.**
- **Before upgrading your BIOS, review the description below to avoid making mistakes, destroying the BIOS and resulting in a non-working system.**

When you encounter problems, for example, you find your system does not support the latest CPU released after our current mainboard, you may therefore upgrade the BIOS, please don't forget to set JAV as close and disable the "Flash Write Protect" item in AWARD BIOS CMOS Setup first (refer to page 26).

Follow the steps exactly for a successful upgrade.

1. Create a bootable system floppy diskette by typing Format A:/s from the DOS prompt under DOS6.xx or Windows 9x environment.
2. Copy AWDFLASH.EXE (version>7.07) from the directory \Utility located on QDI Mainboard Utility CD onto your new bootable diskette.
3. Download the updated BIOS file from the Website (<http://www.qdigrp.com>). Please be sure to download the suitable BIOS file for your mainboard.
4. Uncompress the file download, copy the BIOS file (xx.bin) onto the bootable diskette, and note the checksum of this BIOS which is located in readme file.
5. Reboot the system from the bootable diskette created.
6. Then run the AWDFLASH utility at the A:\ prompt as shown below:

```
A:\AWDFLASH xxxx.bin
```

Follow the instruction through the process. Don't turn off power or reset the system until the BIOS upgrade has been completed.

If you require more detailed information concerning AWDFLASH Utility, for example, the different usage of parameters, please type A:\>AWDFLASH /?



AWARD BIOS Description

Entering Setup

Power on the computer, when the following message briefly appears at the bottom of the screen during the POST (Power On Self Test), press key or simultaneously press the <Ctrl> + <Alt> + <Esc> keys, to enter the AWARD BIOS CMOS Setup Utility.

Press to enter SETUP

Once you have entered, the Main Menu (Figure 1) appears on the screen. The main menu allows you to select from twelve setup functions and two exit choices. Use the arrow keys to select among the items and press the <Enter> key to accept or enter the sub-menu.



Figure-1 Main Menu

Note:The “System Monitor” item will not be displayed if there is no system monitor hardware on the mainboard.

Load Setup Defaults

The Setup Defaults are common and efficient. It is recommended that users load the setup defaults first, then modify the needed configuration settings.

Standard CMOS Setup

The basic CMOS settings included in “Standard CMOS Setup” are Date, Time, Hard Disk Drive Types, Floppy Disk Drive Types, and VGA etc. Use the arrow keys to highlight the item, then use the <PgUp> or <PgDn> keys to select the value you want in each item.

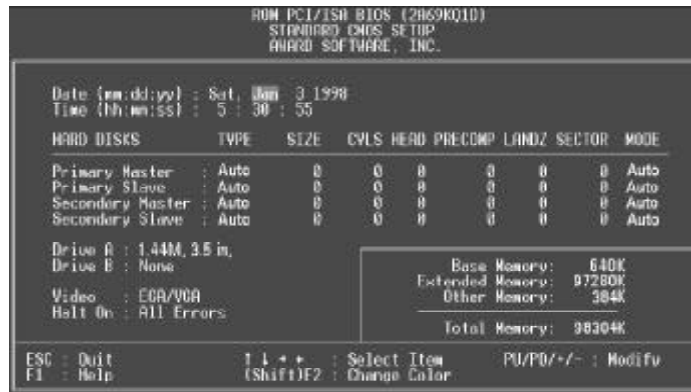


Figure-2 Standard CMOS Setup Menu

Hard Disk

Primary Master/Primary Slave/Secondary Master/Secondary Slave

These categories identify the HDD types of 2 IDE channels installed in the computer system. There are three choices provided for the Enhanced IDE BIOS: None, Auto, and User. “None” means no HDD is installed or set; “Auto” means the system can auto-detect the hard disk when booting up; by choosing “User”, the related information should be entered regarding the following items. Enter the information directly from the keyboard and press <Enter>:

CYLS	number of cylinders	HEAD	number of heads
PRECOMP	write pre-compensation	LANDZ	landing zone
SECTOR	number of sectors	MODE	HDD access mode

Video

Set this field to the type of video display card installed in your system.

EGA/ VGA	Enhanced Graphics Adapter / Video Graphic Array. For EGA, VGA, SEGA, SVGA, or PGA monitor adapters.
CGA 40	Color Graphic Adapter, powering up in 40 column mode.
CGA 80	Color Graphic Adapter, powering up in 80 column mode.
MONO	Monochrome adapter, including high resolution monochrome adapters.



Halt On

This category determines whether or not the computer will stop if an error is detected during powering up.

No errors	The system boot will not stop for any errors that may be detected.
All errors	Whenever the BIOS detects a non-fatal error, the system will stop and you will be prompted.
All, But Keyboard	The system boot will not stop for a keyboard error; but it will stop for all other errors.
All, But Disk/Key	The system boot will not stop for a keyboard or disk error, but it will stop for all other errors.

Memory

This is a Display-Only Category, determined by POST (Power On Self Test) of the BIOS.

Base Memory	The POST of the BIOS will determine the amount of base (or conventional) memory installed in the system.
Extended Memory	The BIOS determines how much extended memory is presented during the POST.
Other Memory	This is the memory that can be used for different applications. Shadow RAM is most used in this area.
Total Memory	Total memory of the system equals the sum of the above memory.



SpeedEasy CPU Setup



Figure-3 SpeedEasy CPU Setup

The following indicates the options of each item and describes their meanings .

<u>Item</u>	<u>Option</u>	<u>Description</u>
• CPU Model	<i>Intel(R)</i> <i>Celeron(TM)</i>	BIOS can automatically detect the CPU model, therefore this item is shown only.
• CPU Speed	<i>200MHz</i> <i>(66x3)</i>	CPU frequency should be set according to the CPU type. For processors with 66MHz front-side bus you can choose from 200MHz (66X3) to 533MHz (66x8). For processors with 100MHz front-side bus, you can select from 300MHz(100X3) to 800MHz(100X8). For processors with 133MHz front-side bus, you can select from 400MHz(133x3) to 1066MHz(133x8).
	<i>Jumper Emulation</i>	This item is only for users who understand all the CPU parameters, i.e. Bus clock and Multiplier. Users are provided with CPU overclock feature through "Jumper Emulation". The host bus speed can be set from 66MHz up to 155MHz. The multiplier can be chosen from 2, 2.5, 3, 3.5, 4, 4.5, 5, 5.5, 6, 6.5, 7, 7.5, 8. However the multiplier setting will not function for bus ratio locked processor, only bus ratio unlocked processor.
• DRAM frequency	<i>Bus clock</i> <i>Bus clock+33MHz</i> <i>Bus clock-33MHz</i>	DRAM frequency same as CPU FSB. (the default is suggested to choose) DRAM frequency is faster than CPU FSB by 33MHz. DRAM frequency is slower than CPU FSB by 33MHz.
• Clock Spread Spectrum	<i>Enabled</i> <i>Disabled</i>	Enables Clock Spread Spectrum to reduce EMI. you can choose 66/83/100/133/140/150MHz host bus speed. Disables Clock Spread Spectrum. you can choose 66/78/100/113/133/144/155MHz host bus speed.

Warning:

Dot not set CPU frequency higher than its working frequency. If you do, we will not be responsible for any damages caused.



BIOS Features Setup



Figure-4 BIOS Features Setup Menu

The following indicates the options for each item and describes their meaning.

<u>Item</u>	<u>Option</u>	<u>Description</u>
• ChipAway Virus On Guard	<i>Enabled</i>	Guards against boot Virus threats early in the boot cycle, before they have a chance to load into your system, ensuring your computer boots to a clean operating system.
	<i>Disabled</i>	Invalidates this function.
• CPU L1/L2 Cache	<i>Enabled</i>	Enables CPU internal Level1/Level2 cache.
	<i>Disabled</i>	Disables CPU internal Level1/Level2 cache.
• CPU L2 Cache ECC	<i>Enabled</i>	Enables CPU L2 Cache ECC (Error Checking and Correction) function.
	<i>Disabled</i>	Disables CPU L2 Cache ECC function.
• Processor Number Feature	<i>Enabled</i>	Pentium®III Processor Number can be readable.
	<i>Disabled</i>	Pentium®III Processor Number can be unreadable.
• Quick Power On Self Test	<i>Enabled</i>	Enables quick POST. BIOS will shorten or skip some check items during POST to speed up POST after you power on the computer.
	<i>Disabled</i>	Normal POST.
• Boot from LAN first	<i>Enabled</i>	Boot from LAN is ahead of any boot sequence selection (LAN adapter must support this function).
	<i>Disabled</i>	Does not boot from LAN first.
• Boot Sequence	<i>C,A,SCSI,...</i> <i>C,CDROM,A</i> <i>LS/ZIP,C</i>	Any search sequency can be chosen for booting.
• Swap Floppy Drive	<i>Enabled</i>	Exchanges the assignment of A&B floppy drives.
	<i>Disabled</i>	The assignment of A&B floppy drives are normal.



• Boot Up Numlock Status	<i>On</i>	Keypad is used as number keys.
	<i>Off</i>	Keypad is used as arrow keys.
• Gate A20 Option	<i>Normal</i>	The A20 signal is controlled by the keyboard controller or chipset hardware.
	<i>Fast</i>	Default setting. The A20 signal is controlled by Port 92 or the chipset specific method.
• Memory Parity/ECC Check	<i>Enabled</i>	Enables the Error Checking & Correction if ECC memory is used.
	<i>Disabled</i>	Disables the ECC function.
• Password Setting	<i>System</i>	The system will not boot and access to BIOS Setup will be denied if the correct password is not entered when prompted.
	<i>Setup</i>	The system will boot up, but access to BIOS Setup will be denied if the correct password is not entered when prompted.
• IDE Second Channel Control	<i>Enabled</i>	Enables the second IDE channel.
	<i>Disabled</i>	Disables the second IDE channel and releases the IRQ.
• OS Select For DRAM>64MB	<i>Non-OS2</i>	If your operating system is not OS/2, please select this item.
	<i>OS2</i>	If system DRAM is more than 64MB and the operating system is OS/2, please select this item.
• HDD S.M.A.R.T Capability	<i>Enabled</i>	Enables S.M.A.R.T hard disk support.
	<i>Disabled</i>	Invalidates this feature.
• Video BIOS Shadow	<i>Enabled</i>	Video BIOS will be copied to RAM. Video Shadow will increase the video speed.
	<i>Disabled</i>	Video shadow is disabled.
• C8000~CBFFF Shadow: DC000-DFFFF	<i>Enabled</i>	Optional ROM will be copied to RAM by 16K bytes per unit.
	<i>Disabled</i>	The shadow function is disabled.
• Delay For HDD 0~15 (Secs):	<i>0~15</i>	Sets the pre-delay time for hard disk to be accessed by the system.
• Show Bootup Logo	<i>Disabled</i>	Enables the logo when system boots up.
	<i>Enabled</i>	Logo will not be shown when system boots up.
• Flash Write Protect	<i>Enabled</i>	Does not allow you to upgrade the BIOS.
	<i>Disabled</i>	Note: Enabling this item can protect the system BIOS from being attacked by severe virus such as CIH. Therefore disable this item only when wanting to flash BIOS, afterwards set this item as Enabled (default). Disabling this item allows you to upgrade the BIOS.



Chipset Features Setup



Figure-5 Chipset Features Setup Menu

The following indicates the options for each item and describes their meaning.

<u>Item</u>	<u>Option</u>	<u>Description</u>
• Bank 0/1, 2/3, 4/5 DRAM Timing	<i>EDO 50ns</i> <i>EDO 60ns</i> <i>Normal</i> <i>Medium</i> <i>Fast</i> <i>Turbo</i>	These items are of selected EDO DRAM read/write timing. Ensure your DIMMs are as fast as 50ns, otherwise select 60ns. The faster you choose, the higher performance you receive.
• SDRAM Cycle Length	<i>2/3</i>	Define the CLT timing parameter of SDRAM expressed in 66MHz clocks. Latency Time = 2 clocks Latency Time = 3 clocks
• Memory Hole	<i>Enabled</i> <i>Disabled</i>	Memory Hole at 15-16M is reserved for expanded ISA card. Do not set this memory hole.
• Read Around Write	<i>Enabled</i> <i>Disabled</i>	Enables read around Write. Disables read around write.
• Concurrent PCI/ HOST	<i>Enabled</i> <i>Disabled</i>	Enables concurrent PCI/Host. Disables concurrent PCI/Host.
• System BIOS Cacheable	<i>Enabled</i> <i>Disabled</i>	Beside conventional memory, system BIOS area is also cacheable. System BIOS area is not cacheable.
• Video RAM Cacheable	<i>Enabled</i> <i>Disabled</i>	Besides conventional memory, video RAM is also cacheable. Video RAM area is not cacheable.
• AGP Aperture Size (MB)	<i>4-256</i>	Sets the effective size of the Graphics Aperture to be used in the particular PAC Configuration.



• AGP-2X Mode	<i>Enabled</i>	Supports 133MHz 2X mode.
	<i>Disabled</i>	Does not support 133MHz 2X mode.
• Onchip USB	<i>Enabled</i>	Enables the onchip USB controller.
	<i>Disabled</i>	Disables the onchip USB controller.
• USB Keyboard Support	<i>Enabled</i>	USB keyboard support is enabled.
	<i>Disabled</i>	USB keyboard support is disabled.
• Close Empty DIMM/PCI Clk	<i>Enabled</i>	Closes empty DIMM or PCI clock to reduce EMI.
	<i>Disabled</i>	Does not close empty DIMM or PCI clock.



Power Management Setup

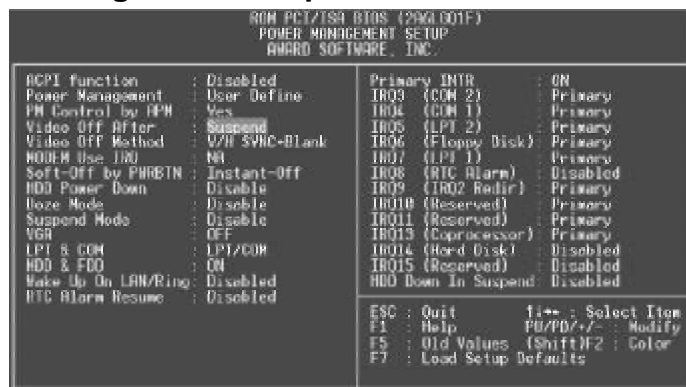


Figure-6 Power Management Setup Menu

The following indicates the options for each item and describes their meaning.

Item	Option	Description
• ACPI function	<i>Enabled</i> <i>Disabled</i>	Validates ACPI function. Invalidates ACPI function.
• Power Management	<i>User Define</i> <i>Min Saving</i> <i>Max Saving</i>	Users can configure their own Power Management Timer. Pre - defined timer values are used. All timers are in their MAX values. Pre - defined timer values are used. All timers are in their MIN values.
• PM Control by APM	No Yes	System BIOS will ignore APM when Power Management is enabled. System BIOS will wait for APM' s prompt before entering any PM mode e.g. Standby or Suspend.
• Video Off After	<i>N/A</i> <i>Suspend</i> <i>Standby</i>	System BIOS will never turn off the screen. Screen blanks after the system enters suspend mode. Screen blanks after the system enters standby mode.
• Video Off Method	<i>Doze</i> <i>Blank Screen</i> <i>V / H SYNC + Blank</i> <i>DPMS</i>	Screen blanks after the system enters Doze mode. The system BIOS will only blank off the screen when disabling video. In addition to Blank Screen, BIOS will also turn off the V-SYNC & H - SYNC signals from VGA cards to monitor. This function is enabled only for the VGA card supporting DPMS.
• Modem Use IRQ	<i>3 ~11</i> <i>NA</i>	Special Wake- up event for the Modem. Invalidates this feature.



• Soft-off by PWRBTN	<i>Instant-off</i>	The system will power off immediately once the power button is pressed.
	<i>Delay 4 Sec</i>	The system will not power off until the power button has been pressed continuously for more than 4 seconds.
• HDD Power Down	<i>Disabled</i> <i>1 ~ 15 Min</i>	Disables HDD Power Down Timer. Defines the continuous HDD idle time before the HDD enters power saving mode (motor off).
• Doze mode	<i>Disabled</i> <i>10Sec ~ 1 Hr</i>	The system never enters Doze mode. Defines the continuous idle time before the system enters Doze mode. If any items defined in "PM Events" are on and activated, the system will be woken up.
• Suspend Mode	<i>Disabled</i> <i>10Sec~ 1Hr</i>	The system never enters Suspend mode. Defines the continuous idle time before the system enters Suspend mode. If any items defined in "PM Events" are on and activated, the system will be woken up.
• VGA	<i>On</i> <i>Off</i>	VGA active reloads global timer. VGA active has no influence to global timer.
• LPT&COM HDD&FDD	<i>LPT/COM</i> <i>OFF/ON</i>	Set the options of these items to reload global timer.
• Wake Up On LAN /Ring	<i>Enabled</i>	Allows the system to be powered on when a remote wake up signal comes up to the WOL header from LAN adapter, or when a ring indicator signal comes up to UART1/UART2 from an external modem or comes up to WOM header from an internal modem card.
• RTC Alarm Resume	<i>Disabled</i>	Does not allow wake up on LAN.
	<i>Enabled</i>	RTC alarm can be used to generate a wake event to power up the system which is in soft power-down status. You can set any date or any time to power up the system.
• Primary INTR IRQ (3-15)	<i>Disabled</i>	RTC has no alarm function.
	<i>Primary</i> <i>Secondary</i>	Reload global timer. No influence to global timer, except finishing an operation that IRQ "X" requests.
• HDD Down In Suspend	<i>Disabled</i>	No influence to global timer.
	<i>Enabled</i>	HDD's motor will be off when the system enters suspend mode.



PNP/PCI Configuration Setup

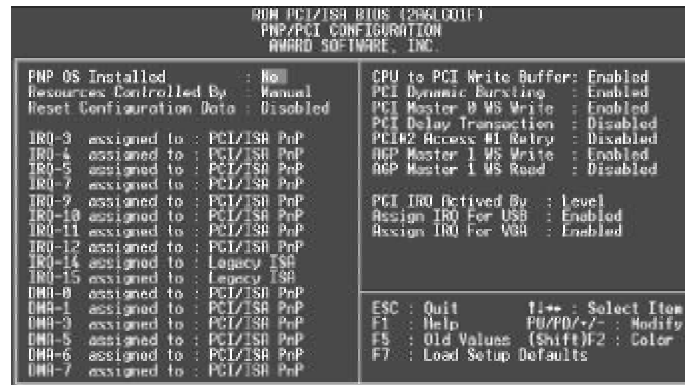


Figure-7 PNP/PCI Configuration Setup Menu

The following indicates the options for each item and describes their meaning.

<u>Item</u>	<u>Option</u>	<u>Description</u>
• PNP OS Installed	Yes No	Device resources assigned by PnP OS. Device resources assigned by BIOS.
• Resources Controlled By	Manual Auto	Assigns the system resources (IRQ and DMA) manually . Assigns system resources (IRQ and DMA) auto- matically by BIOS.
• Reset Configuration Data	Enabled Disabled	The system BIOS will reset configuration data once then automatically set this item as disabled. Disables the configuration data function.
• IRQ-3~IRQ-15 assigned to	Legacy ISA PCI/ISA PnP	The specified IRQ-x will be assigned to ISA only. The specified IRQ-x will be assigned to ISA or PCI.
• DMA-0~DMA-7 assigned to	Legacy ISA PCI/ISA PnP	The specified DMA-x will be assigned to ISA only. The specified DMA-x will be assigned to ISA or PCI.
• CPU to PCI Write Buffer	Enabled Disabled	Enables CPU to PCI Write Buffer. Disables CPU to PCI Write Buffer.
• PCI Dynamic Bursting	Enabled Disabled	Enables PCI Dynamic Bursting. Disables PCI Dynamic Bursting.
• PCI Master 0 ws Write	Enabled Disabled	Enables PCI Master ws Write. Disables PCI Master ws Write.
• PCI Delay Transaction	Enabled Disabled	Enables PCI Delay Transaction. Disables PCI Delay Transaction.
• PCI #2 Access #1 Retry	Enabled Disabled	Enables PCI #2 Access #1 Retry. Disables PCI #2 Access #1 Retry.



- | | | |
|-------------------------|---------------------------------------|--|
| • AGP Master 1 ws Write | <i>Enabled</i>
<i>Disabled</i> | Enables AGP Master 1 ws Write.
Disables AGP Master 1 ws Write. |
| • AGP Master 1 ws Read | <i>Enabled</i>
<i>Disabled</i> | Enables AGP Master 1 ws Read.
Disables AGP Master 1 ws Read. |
| • PCI IRQ Activated By | <i>Level</i>
<i>Edge</i> | Select PCI IRQ Active mode. |
| • Assign IRQ for USB | <i>Enabled</i>

<i>Disabled</i> | Assigns an IRQ for USB. If an USB device is used, enables this item.

Does not assign an IRQ for USB. If no USB device is used, disabling this item can release the IRQ. |
| • Assign IRQ for VGA | <i>Enabled</i>
<i>Disabled</i> | Assigns the needed IRQ for the VGA Card.
Does not assign an IRQ for the VGA card, in order to release the IRQ. |



Integrated Peripherals

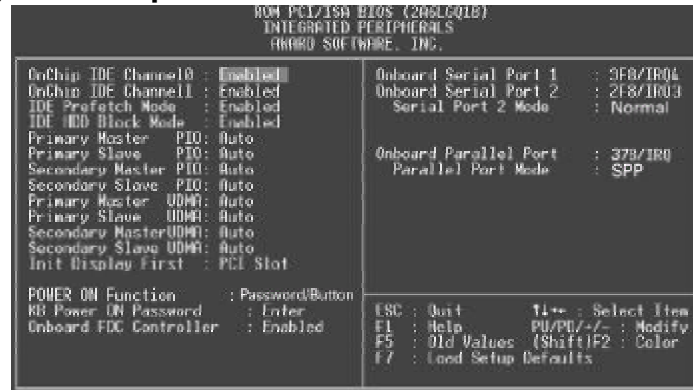


Figure-8 Integrated Peripherals Menu

The following indicates the options for each item and describes their meaning.

<u>Item</u>	<u>Option</u>	<u>Description</u>
• OnChip IDE channel 0/1	<i>Enabled</i> <i>Disabled</i>	Enables OnChip IDE First/Second Channel. Disables OnChip IDE First/Second Channel.
• IDE Prefetch/Mode	<i>Enabled</i> <i>Disabled</i>	Enables IDE Prefetch Mode. Disables IDE Prefetch Mode.
• IDE HDD Block Mode	<i>Enabled</i> <i>Disabled</i>	Allows IDE HDD to read/write several sectors at once. IDE HDD only read/write a sector once.
• IDE Primary/ Secondary Master/Slave PIO	<i>Mode 0 - 4</i> <i>Auto</i>	Defines the IDE primary/secondary master/ slave PIO mode. The IDE PIO mode is defined by auto -detection.
• IDE Primary/ Secondary Master/Slave UDMA	<i>Auto</i> <i>Disabled</i>	Ultra DMA mode will be enabled if an ultra DMA device is detected. Disables this function.
• Init Display First	<i>PCI SLOT</i> <i>AGP</i>	Initializes the PCI VGA first. If a PCI VGA card and an AGP card are installed together in the system, the one initialized first functions. Initializes the AGP first.
• POWER ON FUNCTION	<i>Password/ Button</i> <i>Button Only</i>	Either the power button or the keyboard password can be used to power up the system. Other than choosing this option, the password should be set to implement the keyboard password power-on function. Disables the keyboard password power-on function. The system can be powered on only by the power switch.



	<i>Password</i>	<p>Enables the keyboard password power-on function and disables the power button's power-on function. Other than choosing this option, the password should be set to implement this function.</p> <p>Note: 1. If the option(Password) is chosen, the jumper JKB must be set as pin1&pin2 closed, or you will be unable to power up the system.</p> <p>2. The keyboard password must be set no more than 5 characters and can only use the numbers and alphabetic letters. The password will always remain unless you clear CMOS or reset it.</p>
• Onboard FDC Controller	<i>Enabled</i> <i>Disabled</i>	Onboard floppy disk controller is enabled. Onboard floppy disk controller is disabled.
• Onboard Serial Port 1/2	<i>3F8/IRQ4,</i> <i>2F8/IRQ3,</i> <i>3E8/IRQ4,</i> <i>2E8/IRQ3,</i> <i>Auto</i>	Defines the onboard serial port address and required interrupt number.
• Serial Port 2 Mode	<i>Disabled</i> <i>Standard</i> <i>Sharp IR</i> <i>IrDA SIR</i>	Onboard serial port address and IRQ are automatically assigned Onboard serial port is disabled. Defines Serial Port 2 as standard serial port. Supports SHARP ASK-IR protocol with maximum baud rate up to 57600bps. Supports IrDA version1.0 SIR protocol with maximum baud rate up to 115.2Kbps.
• Onboard Parallel Port	<i>378/IRQ7,</i> <i>278/IRQ5,</i> <i>3BC/IRQ7</i> <i>Disabled</i>	Defines onboard parallel port address and IRQ channel.
• Parallel Port Mode	<i>SPP</i> <i>EPP</i> <i>ECP,</i> <i>ECP+EPP</i>	Onboard parallel port is disabled. Defines the parallel port mode as Standard Parallel Port (SPP), Enhanced Parallel Port (EPP), or Extended Capabilities Port (ECP).



System Monitor

Award BIOS Description	
<pre> ROM PCI/ISA BIOS (286/386/486) System Monitor AWARD SOFTWARE, INC. Current System Temp. : 30°C/86°F Current CPU Temp. : 39°C/102°F Current CPUFAN Speed : 3999 RPM Current CHSFAN Speed : 3998 RPM VCCVID(CPU) Voltage : 1.98V VTT(+1.5V) Voltage : 1.37V +3.3V Voltage : 3.32V +5V Voltage : 4.83V +12V Voltage : 11.79V -12V Voltage : -12.50V VBAT Voltage : 3.00V 5VSB Voltage : 4.95V ESC : Quit F1+/- : Select Item F1 : Help PU/PD/+/- : Modify F5 : Old Values (Shift)F2 : Color F7 : Load Setup Defaults </pre>	

Figure-9 System Monitor Menu

The following describes the meaning of each item.

<u>Item</u>	<u>Current Data Shown</u>	<u>Description</u>
• Current System Temp.	30°C/ 86°F	Temperature inside the chassis.
• Current CPU Temp	39°C/102°C	Temperature of the CPU core.
Current CPUFAN Speed	3999RPM	RPM(Revolution Per Minute) speed of fan connected to the fan header CPUFAN/
Current CHSFAN Speed	3998RPM	CHSFAN. Fan speed value is based on an assumption that tachometer signal is two pulses per revolution; In other cases, you should regard it relatively.
• VCCVID(CPU) Voltage	1.98V	Displays current Voltage values including all significant voltages of the mainboard. +3.3V,
VTT(+1.5V) Voltage	1.37V	+5V, +12V, -12V and 5VSB are voltages from the ATX power supply, VTT (+1.5)
+ 3.3V Voltage	3.32V	Voltage is GTL Termination Voltage from the on-board regulator, and VCCVID (CPU)
+5V Voltage	4.83V	Voltage is the CPU core voltage from the on board switching power supply. VBAT is the
+12V Voltage	11.79V	voltage of the onboard battery.
-12V Voltage	-12.50V	
VBAT Voltage	3.00V	
5VSB Voltage	4.95V	