

Jumper Setting of SR-M504

Bus Clock Speed (JP4, JP 5, JP6)

CPU clock	PCI clock	JP4	JP5	JP6
60Mhz	30 Mhz	on	off	off
66Mhz	33.3Mhz	off	off	off

CPU core/bus clock ratio (JP1, JP2)

Intel Pentium:

Core/Bus Ratio	JP1	JP2
1.5	off	1-2
2	off	2-3
2.5	on	2-3
3	on	1-2

Cyrix 6x86

2	off	2-3
3	off	1-2

AMD

K6/PR2-166	On	2-3
K6/PR2-200	On	1-2
K6/PR2-233	Off	1-2

Common CPU bus clock speed & Core/bus clock ratio

CPU	Multiple	bus clock	JP1	JP2	JP4	JP5	JP6
Intel:							
200MHz	3	66Mhz	on	1-2	off	off	off
180Mhz	3	60Mhz	on	1-2	on	off	off
166Mhz	2.5	66Mhz	on	2-3	off	off	off
150Mhz	2.5	60Mhz	on	2-3	on	off	off
133Mhz	2	66Mhz	off	2-3	off	off	off
Cyrix:							
133Mhz	2	66Mhz	off	2-3	off	off	off
120Mhz	2	60Mhz	off	2-3	on	off	off
AMD:							
AMD PR-rating	AMD CPU		JP1	JP2	JP4	JP5	JP6
100Mhz	100Mhz		off	1-2	off	off	off
120Mhz	90Mhz		off	2-3	on	off	off
133Mhz	100Mhz		off	2-3	off	off	off

150Mhz	120Mhz	On	2-3	on	off	off
166Mhz	133Mhz	On	2-3	off	off	off

CPU Core Voltage

Voltage	JP8
3.52	1-2, 5-6
3.3	5-6
2.9	3-4, 5-6
2.8	7-8

Dual voltage CPU setting

CPU	JP9	JP10
Dual voltage	off	off
Single voltage	on	on

CMOS RAM Clear

This jumper only valid when DS12887A or DS12885A installed as U24

CMOS RAM	JP21
Clear	On
Normal	Off

DIMM supply voltage

DIMM supply voltage	JP24, JP25, JP26
5V	1-2
3.3V	2-3